Hybrid LLC Converter with Wide Range of Zero-Voltage Switching and Wide Input Voltage Operation

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Abstract: A new hybrid inductor-inductor-capacitor (LLC) converter is investigated to have wide voltage input operation capability and zero-voltage turn-on characteristics. The presented circuit topology can be applied for consumer power units without power factor correction or with long hold-up time requirement, photovoltaic energy conversion and renewable energy power transfer. To overcome the weakness of narrow voltage gain of resonant converter, the hybrid LLC converter with different turns ratio of transformer is presented and the experimental investigation is provided to achieve wide voltage input capability (400 V–50 V). On the input-side, the converter can operate as full bridge resonant circuit or half bridge resonant circuit with input split capacitors for high or low voltage input region. On the output-side, the less or more winding turns is selected to overcome wide voltage input operation. According to the circuit structures and transformer turns ratio, the single stage LLC converter with wide voltage input operation capability (400 V–50 V) is accomplished. The laboratory prototype has been developed and the experimental waveforms are measured and demonstrated to investigate the effectiveness of the presented hybrid LLC converter.

Keywords: hybrid LLC converter; wide voltage input; zero-voltage switching; dc-dc converters

1. Introduction

The renewable energy systems have been presented and established to reduce fossil fuel demand and prevent the influence of global warming. The output of renewable energy sources may be unstable voltage or current with dc or ac waveform. Power electronics play the principle of energy transfer between renewable energy sources and energy storage elements or loads. The output of photovoltaic (PV) panel or small-scale wind turbine generator is an unstable and non-constant dc voltage. Therefore, dc converters with wide voltage input capability are normally demanded to achieve the principle of energy transfer between PV panel (or small-scale wind generator) and energy storage unit or dc load. The power switches of dc-dc converters can be controlled by pulse-width modulation (PWM) [1–3] or frequency modulation (FM) [4–6]. In PWM control, the switching frequency is fixed but duty cycle is variable in order to control load voltage. In FM control, the duty cycle is fixed and the switching frequency is variable to control voltage transfer function of dc converters and regulate load voltage. Gallium Nitride (GaN) FET, Insulated Gate Bipolar Transistors (IGBTs), Metal-Oxide-Semiconductor Field-Effect Transistor (MOSFET) and Silicon Carbide (SiC) devices are widely used in power electronics. The main drawback of IGBT devices is low frequency operation. SiC and GaN devices have advantages of high frequency operation and low switching loss for high power density applications. However, SiC and GaN devices have high cost problem compared to MOSFET and IGBT devices. In medium power applications, MOSFET devices have advantages of low cost and medium frequency operation capability.
High frequency and high efficiency power converters are demanded to achieve energy transfer from renewable energy sources to the energy storage units. Due to wide voltage variation of the solar panel output or low power wind generator, the soft switching converters with wide voltage input or output operation have studied. The series-connected dc-dc converter has studied in Reference [7] to accomplish wide input voltage operation for power conversion and battery charger applications. The disadvantage of the series-connected dc-dc converter is poor efficiency. The series/parallel-connected converters with wide voltage operation for vehicles were presented in References [8–11]. The circuit topology needs more active and passive components so that the efficiency and reliability are decreased. Phase-shift PWM scheme with a wide voltage input/output operation were studied in References [12–14]. The main problems of these topologies are the complicated control algorithm, high freewheeling current and hard switching operation at low load condition. Interleaved converters with frequency control have been presented in Reference [15] to have low input ripple current and achieve wide voltage input. This circuit topology is still a multistage circuit. The inductor-inductor-capacitor (LLC) converter with different winding turns was reported in Reference [16] to increase hold-up time on power units of personal computer applications. Four secondary windings are needed so that the conduction loss is increased on the transformer windings. In conventional LLC converter, the circuit can operate well at the limit voltage range to achieve low freewheeling current and high circuit efficiency. If the wide voltage range operation is demanded, the low inductor ratio between the magnetizing inductor and resonant inductor and low quality factor are needed in conventional LLC converter to achieve high voltage gain. However, the magnetizing inductance is decreased and the circulating current loss is increased. Therefore, the LLC converter efficiency is decreased. To solve this problem, a hybrid LLC circuit operation [17] shown in Figure 1a has been proposed to realize 4:1 wide voltage operation \( V_{\text{max}} = 4 V_{\text{min}} \). When input voltage is in low voltage range, the full bridge LLC resonant circuit \((S_1–S_4 \text{ are active,})\) is operated. On the other hand, the half bridge LLC resonant circuit \((S_1 \text{ and } S_2 \text{ are active, } S_3 \text{ OFF and } S_4 \text{ ON})\) is used under high voltage input case. Therefore, wide input voltage operation is achieved in this circuit topology. However, there is a dc voltage level on the resonant capacitor when LLC circuit is operated at half bridge circuit structure. A hybrid LLC converter [18] has been studied to achieve 8:1 wide output voltage operation \( V_{o,\text{max}} = 8 V_{o,\text{min}} \). This circuit topology is based on a full bridge resonant circuit and an ac switch on the primary-side and a hybrid diode rectifier on the output-side. Compared to Reference [17], the half bridge LLC circuit with split input capacitors is used in Reference [18] to avoid a dc voltage level on the resonant capacitor under low voltage output operation. In order to extend output voltage range, a hybrid output rectifier with a full-wave rectification or a half-wave rectification structure is adopted on the secondary-side in Reference [18]. The main disadvantage of this circuit topology in Reference [18] is four diode conduction losses on the secondary-side when full-wave rectification structure is operated.

A new LLC converter is presented to realize wide range of input voltage operation. The LLC resonant circuit is used in the studied converter to achieve soft switching operation for all power devices due to inductive input impedance operation by pulse frequency modulation. To realize wide voltage input operation, the half bridge with split capacitors or full bridge equivalent LLC circuit can be operated on high-voltage side and the voltage doubler rectifier with different secondary turns is adopted on low-voltage side. Due to the different input voltage range, three circuit structures with different voltage gains are controlled to overcome wide range of input voltage variation. The input voltage range of the studied circuit topology is 50 V–400 V. For high voltage input (200 V–400 V), half bridge LLC circuit with fewer winding turns is used to obtain the least voltage gain and regulate load voltage. For medium voltage input (100 V–200 V), full bridge LLC circuit with fewer secondary turns is controlled to attain high voltage gain. For low voltage input (50 V–100 V), full bridge LLC circuit with more winding turns is adopted to obtain the largest voltage gain. Thus, the wide voltage operation (50 V–400 V) is realized in the studied LLC converter. Compared to the multistage converters or series/parallel converters, the proposed converter has simple circuit structure and control scheme by using the general analog integrated circuit. Compared to the 4:1 wide voltage LLC converter in
Reference [17] with 4 switches (3 switches) conduction loss at low (high) voltage range, the proposed converter does not a dc voltage level on the resonant capacitor when half bridge LLC equivalent circuit structure with 4 switches conduction loss is operated and the proposed converter has 8:1 wide voltage operation capability. Compared to the 8:1 wide voltage LLC converter in Reference [18], the proposed converter has only two instead of four diode conduction losses on the secondary-side for high current output applications. The laboratory prototype has been developed and the experiments are given to investigate the presentation of the presented converter.

2. Circuit Configuration

The conventional LLC converters are provided in Figure 1. Figure 1a provides the converter schematic of the full bridge resonant circuit with center-tapped rectification and the voltage gain can be calculated as \( V_o/V_{in} = G_{ac}(f_{sw})n_s/n_p \), where \( G_{ac}(f) \) is the transfer function of resonant tank by \( L_m, L_r \) and \( C_r \). The half bridge resonant circuit with center-tapped rectification is illustrated in Figure 1b. The voltage gain is calculated as \( V_o/V_{in} = G_{ac}(f_{sw})n_s/(2n_p) \). Therefore, the full bridge LLC converter has two times of gain of the half bridge resonant circuit. However, the rectifier diodes \( D_1 \) and \( D_2 \) has at least 2 \( V_o \) voltage stress and more winding turns are needed on secondary-side. Figure 1c,d are the other two circuit configurations with voltage doubler rectification. The voltage gains of Figure 1c,d are obtained as \( V_o/V_{in} = G_{ac}(f_{sw})(2n_s)/n_p \) and \( V_o/V_{in} = G_{ac}(f_{sw})n_s/n_p \), respectively. The voltage rating of \( D_1 \) and \( D_2 \) is \( V_o \) and only one winding set of transformer is used on the output-side. Compared four circuit topologies in Figure 1, the full bridge LLC circuit with voltage double rectification has the largest voltage gain and the half bridge resonant circuit with center-tapped rectification has the least voltage gain.

The proposed converter diagram is shown in Figure 2a to have wide voltage input operation (50 V–400 V) such as solar power or renewable energy conversion. The proposed hybrid LLC converter includes a full bridge resonant circuit (\( S_1–S_4, L_r, C_r \) and \( L_m \)) and a half bridge resonant circuit (\( S_2, S_4, S_{ac1}, L_r, C_r \) and \( L_m \)) on the primary-side and the voltage doubler rectifier with \( N_s \) and 2\( N_s \) winding turns on the secondary-side. The converter has three different operating sub-circuits for three voltage input regions (400 V–200 V, 200 V–100 V and 100 V–50 V) as shown in Figure 2b–d. If each equivalent
circuit in Figure 2b–d is operated to have maximum voltage gain \( G_{\text{max}} = 2 \), then the proposed converter in Figure 2a can achieve wide voltage gain from \( G_{\text{min}} = 1 \) to \( G_{\text{max}} = 8 \). The passive elements \( C_r, L_r \) and \( L_m \) are the basic LLC resonant components. Due to frequency modulation, the LLC resonant tank is controlled to have an inductive input impedance characteristic and the fundamental input voltage of the resonant tank is leading to the fundamental input current. Power devices \( S_1–S_4 \) have soft switching turn-on characteristics. Power devices \( S_{\text{ac}1} \) and \( S_{\text{ac}2} \) are implemented by two back-to-back power MOSFETs. If \( S_{\text{ac}1} \) is ON (OFF), then half bridge (full bridge) resonant circuit is operated on primary-side. \( S_{\text{ac}2} \) is used to determine 2\( N_s \) or \( N_s \) secondary turns connecting to output-side. In the proposed converter, the circuit needs low (or high) voltage gain under high (or low) voltage input case to control load voltage. If \( 400 \text{ V} > V_{\text{in}} > 200 \text{ V} \) (high voltage region \( V_{\text{in-H}} \), Figure 2b, \( S_1, S_2 \) and \( S_{\text{ac}2} \) turn off, \( S_{\text{ac}1} \) turns on and \( S_3 \) and \( S_4 \) are controlled by pulse frequency modulation. The square wave voltage \( v_{ab} \) has voltage amplitudes of \( \pm V_{\text{in}} / 2 \) and the magnetizing inductor voltage \( v_{L_m} \) has voltage amplitudes of \( \pm n_p V_o / (2n_s) \). The voltage gain in Figure 2b is expressed as \( V_o / V_{\text{in-H}} = G_{\text{ac}}(f_{\text{sw}}) n_s / n_p \), where \( G_{\text{ac}}(f) \) is the transfer function of the resonant tank by \( L_m, C_r, \) and \( L_r \). If \( 200 \text{ V} > V_{\text{in}} > 100 \text{ V} \) (medium voltage region \( V_{\text{in-M}} \), Figure 2c, \( S_{\text{ac}1} \) and \( S_{\text{ac}2} \) turn off and \( S_1–S_4 \) are controlled by pulse frequency modulation. The square wave voltage \( v_{ab} \) has voltage amplitudes of \( \pm V_{\text{in}} \) and the magnetizing inductor voltage \( v_{L_m} \) has voltage amplitudes of \( \pm n_p V_o / (4n_s) \). The voltage gain is \( V_o / V_{\text{in-M}} = G_{\text{ac}}(f_{\text{sw}}) (4n_s) / n_p \). From the obtained voltage gains in high, medium and low voltage input regions, it is observed that the circuit has the largest gain \( G_{\text{ac}}(f_{\text{sw}}) (4n_s) / n_p \) at high voltage input region (200 V–400 V).

![Figure 2](image-url)
Lm and Ro, the voltage gain under high voltage input region is $V_o/V_{in-H} = n_s G_{ac}(f_{sw})/n_p$, where $G_{ac}(f)$ is the transfer function by $L_r$, $C_r$, $L_m$, and $R_o$. The LLC converter has four or six operating modes under $f_{sw}$ (switching frequency) > or < $f_r$ (resonant frequency). Since the converter is designed to have wide voltage gain, $f_{sw}$ is always less than or close to $f_r$. Therefore, the zero-voltage switching (ZVS) of $S_3$ and $S_4$ and zero-current switching (ZCS) of $D_3$ and $D_4$ are accomplished. The PWM waveforms of LLC converter operated for high voltage region are illustrated in Figure 3a and the Figure 3b–g provide the equivalent circuits for modes 1–6 for one switching cycle.

a. **Mode 1** [$t_0, t_1$]: At $t_0$, $v_{CS4} = 0$. The current $i_{Lr}$ flows through $D_4$ and $S_4$ turns on at this instant to have ZCS operation. The secondary current is positive so that $D_3$ is conducting and capacitor $C_{o1}$ is charged. The components $C_r$ and $L_r$ are naturally resonant and the resonant frequency $f_r \approx 1/(2\pi \sqrt{L_r C_r})$.

b. **Mode 2** [$t_1, t_2$]: Since $f_r > f_{sw}$, $i_{D3}$ is decreased to zero at $t_1$ with ZCS operation and $i_{Lr}(t_1) = i_{Lm}(t_1)$. Thus, the load current $I_o$ will discharge $C_{o1}$ and $C_{o2}$. The current $i_{Lm}(t_2)$ is obtained $i_{Lm}(t_2) \approx (n_p V_o)/(8n_s L_m f_{sw})$.

c. **Mode 3** [$t_2, t_3$]: Power device $S_4$ turns off at $t_2$. $i_{Lr}(t_2)$ will discharge (charge) $C_{S3}$ ($C_{S4}$). Due to $i_{Lr}(t_2) < i_{Lm}(t_2)$, $D_4$ will naturally conduct and capacitor voltage $v_{C_{o2}}$ is increased.

d. **Mode 4** [$t_3, t_4$]: At the start of this mode, the capacitor voltage $v_{CS3} = 0$. $i_{Lr}(t_3)$ is positive and $D_3$ is conducting so that $S_3$ can turn on at this instant to accomplish ZVS. In mode 4, the resonant tank transfers power from input terminal $V_{in}$ to output terminal $V_o$.

e. **Mode 5** [$t_4, t_5$]: The diode current $i_{D4}$ will naturally turn off at $t_4$ due to $f_r > f_{sw}$. In this mode, the load current $I_o$ discharges $C_{o1}$ and $C_{o2}$. The current $i_{Lm}(t_5)$ is obtained $i_{Lm}(t_5) \approx -(n_p V_o)/(8n_s L_m f_{sw})$.

f. **Mode 6** [$t_5, T_{sw} + t_0$]: $S_3$ turns off at the start of this mode. $i_{Lr}(t_5)$ discharge (charge) $C_{S4}$ ($C_{S3}$) and $D_3$ start conducting. To achieve zero-voltage switching of $S_4$, $|i_{Lm}(t_5)|$ must be greater than $V_{in-H} \sqrt{2C_{oss}/(L_m + L_r)}$. At the end of this mode, $v_{CS4} = 0$. 

![Figure 3](image-url)
Figure 3. Presented converter under high voltage input region (a) pulse-width modulation (PWM) signals (b) mode 1 equivalent circuit (c) mode 2 equivalent circuit (d) mode 3 equivalent circuit (e) mode 4 equivalent circuit (f) mode 5 equivalent circuit (g) mode 6 equivalent circuit.

When $V_{in}$ is in medium voltage region ($V_{in-M}$: 100 V–200 V), $S_{ac1}$ and $S_{ac2}$ turn off. The full bridge LLC circuit and low secondary turns are used in Figure 2c. The FM scheme is used to control $S_1$–$S_4$. The converter has gain $V_o/V_{in-M} = 2G_{ac}(f_{sw})n_{s}/n_p$. The PWM signals and the corresponding circuits are illustrated in Figure 4 for medium voltage input condition.

a. **Mode 1** [$t_0$, $t_1$]: This mode starts at $t_0$ if the drain voltages $v_{S4,d} = v_{S1,d} = 0$. $i_L$ flows through $D_{S4}$ and $D_{S1}$ so that $S_4$ and $S_1$ can turn on with ZVS operation. Since $i_{Lr}(t_0) > i_{Lm}(t_0)$, it can observe that $D_3$ conducts, $v_{Lm} \approx (n_p V_o)/(2n_s)$. The resonant frequency by passive elements $L_r$ and $C_r$ in mode 1 is $f_r = 1/[2\pi \sqrt{L_r C_r}]$. The load current $I_o$ discharges $C_{o2}$ and $i_{D3}$ charges $C_{o1}$.

b. **Mode 2** [$t_1$, $t_2$]: If $f_{sw} > f_r$, $D_3$ will naturally turn off at $t_1$. This mode ends at $t_2$ and $i_{Lm}(t_2) \approx (n_p V_o)/(8n_s L_m f_{sw})$.

c. **Mode 3** [$t_2$, $t_3$]: At $t_2$, $S_1$ and $S_3$ turn off. $i_{Lr}(t_2)$ discharges $C_{S3}$ and $C_{S2}$. Since $i_{Lm}(t_2) > i_{Lr}(t_2)$, the diode $D_4$ is forward biased. To realize ZVS turn-on of $S_3$ and $S_2$, the current $i_{Lm}(t_2)$ must be greater than $V_{in,M} \sqrt{C_{oss}}/(L_m + L_r)$.

d. **Mode 4** [$t_3$, $t_4$]: At $t_3$, $C_{S3}$ and $C_{S2}$ are discharged to zero. $i_{Lr}(t_3)$ is positive so that $D_{S2}$ and $D_{S3}$ are conducting. Thus, the zero-voltage switching of $S_3$ and $S_2$ can be achieved after time $t_3$. Owing to $i_{Lm}(t_3) > i_{Lr}(t_3)$, $D_4$ conducts, $v_{Lm} \approx -(V_o n_p)/(2n_s)$ and $i_{Lm}$ decreases. $C_{o2}$ is charged.

e. **Mode 5** [$t_4$, $t_5$]: Since $f_r > f_{sw}$, $D_4$ will naturally turn off at $t_4$. The current $i_{Lm}(t_5)$ is equal to $-(n_p V_o)/(8n_s L_m f_{sw})$.

f. **Mode 6** [$t_5$, $T_{sw} + t_0$]: At $t_5$, power devices $S_3$ and $S_2$ are turned off. Since $i_{Lr}(t_5) < 0$, $C_{S4}$ and $C_{S1}$ discharge in this mode. To realize ZVS turn-on of $S_4$ and $S_1$, the current $|i_{Lm}(t_5)|$ must be greater than $V_{in,M} \sqrt{C_{oss}}/(L_m + L_r)$. The mode 6 ends at time $T_{sw} + t_0$ when $v_{CS1} = v_{CS4} = 0$. 


Figure 4. Presented converter under medium voltage input region (a) PWM signals (b) mode 1 equivalent circuit (c) mode 2 equivalent circuit (d) mode 3 equivalent circuit (e) mode 4 equivalent circuit (f) mode 5 equivalent circuit (g) mode 6 equivalent circuit.

If $50 \, \text{V} < V_{\text{in}} < 100 \, \text{V}$ (low voltage region, $V_{\text{in-L}}$), $S_{\text{ac1}}$ turns off and $S_{\text{ac2}}$ turns on. The LLC circuit and $2N_\text{s}$ secondary turns are used in Figure 2d. For low voltage input region, the converter has gain $V_0/V_{\text{in-L}} = 4G_{\text{ac}}(f_{\text{sw}})n_\text{s}/n_\text{p}$. It can observe the proposed converter under low voltage input region has the largest voltage gain compared to the high and medium voltage regions. Figure 5 illustrates the PWM signals and the corresponding mode circuits.

a. **Mode 1** [$t_0, t_1$]: At time $t_0$, $v_{\text{CS4}} = v_{\text{CS1}} = 0$. $D_{\text{S4}}$ and $D_{\text{S1}}$ turn on due to $i_{\text{Lr}}(t_0) < 0$. The zero-voltage switching of $S_{\text{S4}}$ and $S_{\text{S1}}$ are achieved after time $t_0$. $i_{\text{Lr}}(t_0) > i_{\text{Lm}}(t_0)$ and $D_1$ is conducting.

b. **Mode 2** [$t_1, t_2$]: Since $f_r > f_{\text{sw}}$, $D_1$ will naturally turn off at $t_1$ and $i_{\text{Lr}}(t_1) = i_{\text{Lm}}(t_1)$. The load current $i_0$ discharges $C_{\text{S1}}$ and $C_{\text{S2}}$. $C_r$, $L_m$ and $L_r$ are naturally resonant with frequency $f_p$.

c. **Mode 3** [$t_2, t_3$]: At $t_2$, $S_3$ and $S_1$ turn off. $i_{\text{Lr}}(t_2)$ discharges $C_{\text{S3}}$ and $C_{\text{S2}}$. Since $i_{\text{Lr}}(t_2) < i_{\text{Lm}}(t_2)$, $D_2$ is forward biased and $v_{\text{C02}}$ is increased.
d. **Mode 4 \([t_3, t_4]\):** Mode 4 starts at \(t_3\) when \(C_{S3}\) and \(C_{S2}\) are discharged to zero voltage. \(i_{Lr}(t_3)\) is positive so that \(D_{S3}\) and \(D_{S2}\) are conducting. \(S_3\) and \(S_2\) turn on at this moment to have ZVS operation.

e. **Mode 5 \([t_4, t_5]\):** If \(f_r > f_{sw}\), \(D_2\) will turn off with zero-current switching at \(t_4\). The load current \(I_o\) discharges \(C_{o1}\) and \(C_{o2}\).

f. **Mode 6 \([t_5, T_{sw} + t_0]\):** This mode starts at \(t_5\), when \(S_3\) and \(S_2\) turn off. \(i_{Lr}(t_5)\) will discharge (charge) \(C_{S4}\) and \(C_{S1}\) and \(D_1\) is forward biased. This mode ends at \(T_{sw} + t_0\) when \(v_{CS4} = v_{CS1} = 0\).

![Figure 5](image.png)

**Figure 5.** Presented converter under low voltage input region (a) PWM signals (b) mode 1 equivalent circuit (c) mode 2 equivalent circuit (d) mode 3 equivalent circuit (e) mode 4 equivalent circuit (f) mode 5 equivalent circuit (g) mode 6 equivalent circuit.

4. **Circuit Characteristics and Design Example**

Based on the input voltage regions, \(S_{ac1}\) and \(S_{ac2}\) are controlled at on or off state. Therefore, the converter can be operated at three input voltage regions. Full bridge (half bridge) LLC converter is operated to have low (high) voltage gain for low (high) voltage input region. The equivalent resonant
circuit is derived in Figure 6a. According to the PWM signals of S₁–S₄, the square wave voltage $V_{ac}$ is generated. If full bridge LLC converter with S₁–S₄ is operated, the voltage $V_{ac}$ has voltage values $\pm V_{in}$ in Figure 2c,d. If the half bridge LLC converter with S₂ and S₃ shown in Figure 2b is operated, the voltage $V_{ac}$ has voltage values $\pm V_{in}/2$. For medium and high voltage input regions shown in Figure 2b,c, $S_{ac2}$ is off. It can obtain the inductor voltage $V_{Lm} = nV_{o}/2$ or $-nV_{o}/2$, where $n = n_p/n_s$, if $D_3$ or $D_4$ is conducting. For low voltage input region (Figure 2d), the inductor voltage $V_{Lm} = nV_{o}/4$ or $-nV_{o}/4$ if $D_1$ or $D_2$ is conducting. In Figure 6a, the voltage $V_{ac}$ has voltage values $\pm V_{in}/2$. For medium and high voltage input regions shown in Figure 2b,c, $S_{ac2}$ is off. It can obtain the inductor voltage $V_{Lm} = nV_{o}/2$ or $-nV_{o}/2$, where $n = n_p/n_s$, if $D_3$ or $D_4$ is conducting. For low voltage input region (Figure 2d), the inductor voltage $V_{Lm} = nV_{o}/4$ or $-nV_{o}/4$ if $D_1$ or $D_2$ is conducting. In Figure 6a, the input-side and output-side of the resonant tank are all square voltage waveforms. Based on the Fourier Series Analysis (FSA), the square voltage waveform is expressed as the summation of the fundamental frequency component and the higher order harmonic frequency components. Thus, the circuit analysis of the resonant circuit in Figure 6a is related to the nonlinear and nonsinusoidal circuit analysis. If the output voltage at higher order harmonic frequencies are less than the fundamental harmonic voltage, then only the fundamental harmonic of square voltage wave is considered in Figure 6b to simplify the circuit analysis and design the resonant converter. The fundamental harmonic approximation (FHA) approach has acceptable design results if the LLC converter is operated at or close to the resonant frequency $f_r$. In Figure 6b, the rms values of fundamental harmonic voltage at input and output side are given as.

$$V_{ac,f} = \begin{cases} 2\sqrt{2}V_{in}/\pi, S_{ac,1} \text{ off} \\ \sqrt{2}V_{in}/\pi, S_{ac,1} \text{ on} \end{cases}$$

(1)

### Figure 6. The equivalent resonant circuit of the proposed LLC converter with (a) nonlinear nonsinusoidal circuit (b) linear sinusoidal circuit.

The ac equivalent load resistance $R_{e,ac}$ can be obtained according to power balance between primary and secondary sides of transformer.

$$P_{ac} = (V_{Lm,f})^2/R_{e,ac} = P_o = (V_o)^2/R_o.$$  \hspace{1cm} (3)

From Equations (2) and (3), the ac equivalent load resistance is calculated as.

$$R_{e,ac} = \begin{cases} 2n^2R_o/(\pi^2), S_{ac2} \text{ off} \\ nR_o/(2\pi^2), S_{ac2} \text{ on} \end{cases}$$

(4)

From the linear circuit in Figure 6b, the voltage transfer function can be approximated by using the fundamental input and output voltages.

$$G_{ac}(f_{sw}) = \left[(jX_{LM}/R_{e,ac})\left[(jX_{LM}/R_{e,ac})+jX_{Lr}-jX_{Cr}\right] = (V_{Lm,f})^2/(V_{ac,f})^2,$$ \hspace{1cm} (5)

where $X_{LM} = 2\pi f_{sw}L_m$, $X_{Lr} = 2\pi f_{sw}L_r$ and $X_{Cr} = 1/(2\pi f_{sw}C_r)$. From Equations (1), (2) and (5), the voltage transfer function $G_{ac}(f_{sw})$ is obtained in Equations (6)–(8) for high, medium and low voltage input regions.
\[ G_{ac}(f_{sw}) = \frac{1}{\sqrt{1 + \left(\frac{f_{sw}}{f_{r}}\right)^2 - 1}} = \frac{nV_o}{V_{in-H}} \]  
(6)

\[ G_{ac}(f_{sw}) = \frac{1}{\sqrt{1 + \left(\frac{f_{sw}}{f_{r}}\right)^2 - 1}} = \frac{nV_o}{2V_{in-M}} \]  
(7)

\[ G_{ac}(f_{sw}) = \frac{1}{\sqrt{1 + \left(\frac{f_{sw}}{f_{r}}\right)^2 - 1}} = \frac{nV_o}{4V_{in-L}} \]  
(8)

Therefore, the load voltage \( V_o \) can be obtained under different input voltage conditions and expressed in Equations (9)–(11) for high, medium and low voltage input regions, respectively.

\[ V_o = \frac{V_{in-H}}{n} \left[1 + \left(\frac{f_{sw}}{f_{r}}\right)^2 - 1\right]^{1/2} \left[1 + \left(\frac{L_m / f_{sw}}{L_{r,ac}}\right)^2 - 1\right]^{1/2} \]  
(9)

\[ V_o = \frac{2V_{in-M}}{n} \left[1 + \left(\frac{f_{sw}}{f_{r}}\right)^2 - 1\right]^{1/2} \left[1 + \left(\frac{L_m / f_{sw}}{L_{r,ac}}\right)^2 - 1\right]^{1/2} \]  
(10)

\[ V_o = \frac{4V_{in-L}}{n} \left[1 + \left(\frac{f_{sw}}{f_{r}}\right)^2 - 1\right]^{1/2} \left[1 + \left(\frac{L_m / f_{sw}}{L_{r,ac}}\right)^2 - 1\right]^{1/2} \]  
(11)

The studied LLC converter is operated under wide voltage input (50–400 V), the load voltage \( V_o \) is 48 V and load power is 500 W. The resonant frequency \( f_r \) is designed at 100 kHz. The selected magnetizing and resonant inductances are \( L_m = 6L_r \). For high (\( V_{in-H}, 200 V–400 V \)), medium (\( V_{in-M}, 100 V–200 V \)) and low (\( V_{in-L}, 50 V–100 V \)) voltage input ranges, the equivalent resonant circuit models in Figure 6b are identical. Thus, the converter operated at high voltage input region is designed in the following discussion. According to Equation (6), it is clear that the proposed converter has the minimum voltage gain at \( V_{in} = 400 V \) and has the maximum voltage gain at \( V_{in} = 200 V \). It is assumed that the dc gain at \( V_{in} = 400 V \) input is designed as 0.95. From Equation (6), the primary-secondary turn \( n = n_p / n_s \) is obtained in Equation (12).

\[ n = n_p / n_s = G_{ac}(f_{sw})V_{in,max} / V_o = 0.95 \times 400 / 48 = 7.916. \]  
(12)

The magnetic core TDK EE-55 with \( n = n_p / n_s = 16 / 2 = 8 \) is used to implement transformer \( T \). Therefore, the voltage gains at \( V_{in} = 400 V \) and 200 V are calculated in Equations (13) and (14).

\[ G_{ac, min} = nV_o / V_{in,min} = 8 \times 48 / 400 = 0.96. \]  
(13)

\[ G_{ac, max} = nV_o / V_{in,min} = 8 \times 48 / 200 = 1.92. \]  
(14)

For high voltage input range, \( S_{ac2} \) is off. From Equation (4), the ac equivalent load resistance is calculated as.

\[ R_{e,ac} = 2n^2R_o / \pi^2 = 2 \times 8^2 \times (482 / 500) / \pi^2 \approx 60 \Omega. \]  
(15)

The selected resonant inductance \( L_r = 7 \mu H \) so that the magnetizing inductance \( L_m = 6L_r = 42 \mu H \). The resonant capacitance \( C_r \) is calculated in Equation (16).
\[ C_r = \frac{1}{(4\pi^2L_r f_s^2)} = \frac{1}{(4 \times \pi^2 \times 7 \times 10^{-6} \times 10^5 \times 10^5)} \approx 360 \text{ nF}. \] (16)

MOSFETs TK50J60U with 600 V/25 A ratings are used for power switches \(S_1-S_4\) and \(S_{ac1}\). IXTP160N075T with 75 V/160 A ratings is used for power switch \(S_{ac2}\). Power diodes MBR40100PT with 100 V/40 A ratings are adopted for the rectifier diodes \(D_1-D_4\). The input and output capacitances are \(C_{in1} = C_{in2} = 300 \mu\text{F}\) and \(C_{o1} = C_{o2} = 540 \mu\text{F}\). Two Schmitt voltage comparators are used to control PWM signals of \(S_{ac1}\) and \(S_{ac2}\). The reference voltages of two comparators are designed at 200 V (between medium and high voltage input regions) and 100 V (between low and medium voltage input regions). Table 1 shows the specifications of the active and passive components in the studied LLC converter.

5. Experimental Verification of the Presented Converter

The hardware circuit of the presented circuit shown in Figure 7 is setup and the circuit specifications are shown in Table 1. The prototype is tested and the measured waveforms are provided to confirm the effectiveness of the presented resonant converter. The PWM signals of active switches at 50 V input (low voltage input region), 190 V input (medium voltage input region) and 400 V input (high voltage input region) are shown in Figure 8a–c respectively. For \(V_{in} = 50\) V input (low voltage range), \(S_{ac1}\) is off, \(S_{ac2}\) is on and \(S_1-S_4\) are active with switching frequency \(f_{sw} \approx 65\) kHz shown in Figure 8a. For \(V_{in} = 190\) V input (medium voltage range), \(S_{ac1}\) and \(S_{ac2}\) are off and only \(S_1-S_4\) are active with switching frequency \(f_{sw} \approx 102\) kHz (Figure 8b). For \(V_{in} = 400\) V input (high voltage range), \(S_{ac1}\) is on and \(S_1, S_2\) and \(S_{ac2}\) are off. Only \(S_3\) and \(S_4\) are active with switching frequency \(f_{sw} \approx 108\) kHz shown in Figure 8c. Figures 9 and 10 give the experimental waveforms of input-side and output-side, respectively, at \(V_{in} = 50\) V, 190 V and 400 V under full load. For 50 V input (low voltage range), \(S_{ac2}\) is on so that \(D_1\) and \(D_2\) are active and \(D_3\) and \(D_4\) are in the off state as shown in Figure 10a. For 190 V and 400 V input conditions, \(S_{ac2}\) is off so that \(D_1\) and \(D_2\) are reverse biased and \(D_3\) and \(D_4\) are active (Figure 10b,c). From the measured waveforms in Figure 10, the output capacitor voltages \(V_{Co1}\) and \(V_{Co2}\) are balanced well. Figure 11 gives the PWM signals of power switch \(S_1\) or \(S_2\) at 50 V, 190 V and 400 V input under 20% and 100% loads. It can observe the ZVS operation of \(S_1\) and \(S_3\) is realized for both 20% and 100% loads. Figure 12a shows the input voltage \(V_{in}\) and the gating voltage \(v_{Sac2,g}\) between \(V_{in} = 50\) V and 400 V. When \(V_{in} \geq 0\) or \(< 100\) V, \(S_{ac2}\) is off (medium or high voltage range) or on (low voltage range). Figure 12b provides the input voltage \(V_{in}\) and PWM voltages \(v_{Sac1, g}, v_{S1, g}\) and \(v_{S2, g}\) between \(V_{in} = 0\) V and 400 V. When \(V_{in} \geq 200\) V (high voltage input region), \(S_{ac1}\) is turned on and \(S_1\) and \(S_2\) are turned off. The converter efficiencies for different input voltages and loads are given in Table 2. Under low voltage input region, the 2\(n_1\) winding turns are used on the output-side. Therefore, the higher primary-side current is introduced and the conduction losses on switches and winding turns are increased compared to the 2\(n_2\) winding turns used on the output-side under medium and high voltage input regions. Thus, the circuit efficiency at low voltage input region is less than the medium or high voltage input region under the same load. The measured switching frequency at different loads and input conditions are provided in Table 3. On each input voltage region, the higher input voltage results in the higher switching frequency under the same load.

![Figure 7. Presented LLC converter (a) picture of laboratory prototype (b) picture of the experiment setup.](image-url)
Table 1. Specifications of the presented converter.

<table>
<thead>
<tr>
<th>Items</th>
<th>Parameter</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltage $V_{\text{in}}$</td>
<td>50 V–400 V</td>
</tr>
<tr>
<td>Output voltage $V_o$</td>
<td>48 V</td>
</tr>
<tr>
<td>Rated power $P_o$</td>
<td>500 W</td>
</tr>
<tr>
<td>Resonant frequency $f_r$</td>
<td>100 kHz</td>
</tr>
<tr>
<td>Input capacitances $C_{\text{in}1}, C_{\text{in}2}$</td>
<td>300 µF/400 V</td>
</tr>
<tr>
<td>Output capacitances $C_{\text{o}1}, C_{\text{o}2}$</td>
<td>540 µF/100 V</td>
</tr>
<tr>
<td>Resonant capacitance $C_r$</td>
<td>360 nF</td>
</tr>
<tr>
<td>Resonant inductance $L_r$</td>
<td>7 µH</td>
</tr>
<tr>
<td>Power switches $S_1–S_4, S_{\text{ac}1}$</td>
<td>TK50J60U</td>
</tr>
<tr>
<td>Power switch $S_{\text{ac}2}$</td>
<td>DXP160N075T</td>
</tr>
<tr>
<td>Rectifier diodes $D_1–D_4$</td>
<td>MBR40100PT</td>
</tr>
<tr>
<td>Winding turns of $T$: $n_p, n_s, n_h$</td>
<td>16, 2, 2</td>
</tr>
<tr>
<td>Magnetizing inductance $L_m$</td>
<td>42 µH</td>
</tr>
</tbody>
</table>

Figure 8. PWM signals of switches at full load and (a) $V_{\text{in}} = 50$ V (low voltage input region) (b) $V_{\text{in}} = 190$ V (medium voltage input region) (c) $V_{\text{in}} = 400$ V (high voltage input region).
Figure 9. Input ac voltage $V_{ab}$, capacitor voltage $v_{Cr}$ and inductor current $i_{Lr}$ at full load and (a) $V_{in} = 50$ V (low voltage input region) (b) $V_{in} = 190$ V (medium voltage input region) (c) $V_{in} = 400$ V (high voltage input region).

Figure 10. Output diode currents and capacitor voltages at full load and (a) $V_{in} = 50$ V (low voltage input region) (b) $V_{in} = 190$ V (medium voltage input region) (c) $V_{in} = 400$ V (high voltage input region).
Figure 11. Measured PWM signals of power switch (a) $S_1$ at $V_{in} = 50$ V (low voltage range) and 20% load (b) $S_1$ at $V_{in} = 50$ V (low voltage range) and full load (c) $S_1$ at $V_{in} = 190$ V (medium voltage range) and 20% load (d) $S_1$ at $V_{in} = 190$ V (medium voltage range) and full load (e) $S_3$ at $V_{in} = 400$ V (high voltage range) and 20% load (f) $S_3$ at $V_{in} = 400$ V (high voltage range) and full load.

Figure 12. Measured waveforms (a) $V_{in}$ and $v_{Sac2}$ (b) $V_{in}$, $v_{Sac1}$, $v_{S1,g}$ and $v_{S2,g}$. 
Table 2. Measured efficiencies under input voltage and load conditions.

<table>
<thead>
<tr>
<th>Input Voltage $V_{\text{in}}$</th>
<th>Efficiency</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{\text{in}} = 50$ V &amp; 50% load (low voltage range)</td>
<td>88.1%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 50$ V &amp; 100% load (low voltage range)</td>
<td>87.2%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 90$ V &amp; 50% load (low voltage range)</td>
<td>89.7%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 90$ V &amp; 100% load (low voltage range)</td>
<td>88.1%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 110$ V &amp; 50% load (medium voltage range)</td>
<td>89.5%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 110$ V &amp; 100% load (medium voltage range)</td>
<td>88.2%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 190$ V &amp; 50% load (medium voltage range)</td>
<td>91.3%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 190$ V &amp; 100% load (medium voltage range)</td>
<td>90.1%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 210$ V &amp; 50% load (high voltage range)</td>
<td>90.3%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 210$ V &amp; 100% load (high voltage range)</td>
<td>89.6%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 400$ V &amp; 50% load (high voltage range)</td>
<td>92.5%</td>
</tr>
<tr>
<td>$V_{\text{in}} = 400$ V &amp; 100% load (high voltage range)</td>
<td>91.8%</td>
</tr>
</tbody>
</table>

Table 3. Measured switching frequencies under input voltage and load conditions.

<table>
<thead>
<tr>
<th>Input Voltage $V_{\text{in}}$</th>
<th>Switching Frequency $f_{\text{sw}}$ (kHz)</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{\text{in}} = 50$ V &amp; 50% load (low voltage range)</td>
<td>61</td>
</tr>
<tr>
<td>$V_{\text{in}} = 50$ V &amp; 100% load (low voltage range)</td>
<td>52</td>
</tr>
<tr>
<td>$V_{\text{in}} = 90$ V &amp; 50% load (low voltage range)</td>
<td>85</td>
</tr>
<tr>
<td>$V_{\text{in}} = 90$ V &amp; 100% load (low voltage range)</td>
<td>81</td>
</tr>
<tr>
<td>$V_{\text{in}} = 110$ V &amp; 50% load (medium voltage range)</td>
<td>57</td>
</tr>
<tr>
<td>$V_{\text{in}} = 110$ V &amp; 100% load (medium voltage range)</td>
<td>55</td>
</tr>
<tr>
<td>$V_{\text{in}} = 190$ V &amp; 50% load (medium voltage range)</td>
<td>101</td>
</tr>
<tr>
<td>$V_{\text{in}} = 190$ V &amp; 100% load (medium voltage range)</td>
<td>98</td>
</tr>
<tr>
<td>$V_{\text{in}} = 210$ V &amp; 50% load (high voltage range)</td>
<td>57</td>
</tr>
<tr>
<td>$V_{\text{in}} = 210$ V &amp; 100% load (high voltage range)</td>
<td>54</td>
</tr>
<tr>
<td>$V_{\text{in}} = 400$ V &amp; 50% load (high voltage range)</td>
<td>104</td>
</tr>
<tr>
<td>$V_{\text{in}} = 400$ V &amp; 100% load (high voltage range)</td>
<td>97</td>
</tr>
</tbody>
</table>

6. Conclusions

A new ZVS LLC converter is presented, analysis and realized to have wide load soft switching and wide voltage input operation. In order to accomplish wide load range of ZVS operation, the LLC resonant tank is used to accomplish ZVS turn-on for all active devices. According to the different input voltage ranges (50 V–400 V), the variable voltage gain is used to regulate load voltage constant. To realize the variable voltage gain which is related to the input voltage range (low, medium or high voltage range), two ac power switches are adopted in order to select half bridge or full bridge resonant circuit with the different turns-ratio of transformer. Thus, the wide range of input voltage operation is realized in the proposed circuit. The studied LLC converter can be applied to wide voltage input variation such as switching power converters with long hold-up time requirement, battery systems or solar panel. Compared to the other wide voltage circuit topologies, the proposed converter has no dc voltage level on the resonant converter [17] and less diode conduction loss [18]. However, more power devices are still needed in the proposed converter to realize 8:1 wide voltage operation. Thus, the cost issue is the main drawback of the studied circuit for industry applications. The presented converter
has been developed in the laboratory and its effectiveness was validated through experiments by a laboratory prototype circuit.

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**Conflicts of Interest:** The authors declare no conflict of interest.

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