

Article



# An Efficient Backward/Forward Sweep Algorithm for Power Flow Analysis through a Novel Tree-Like Structure for **Unbalanced Distribution Networks**

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Abstract: The increase of distributed energy resources (DERs) in low voltage (LV) distribution networks requires the ability to perform an accurate power flow analysis (PFA) in unbalanced systems. The characteristics of a well performing power flow algorithm are the production of accurate results, robustness and quick convergence. The current study proposes an improvement to an already used backward-forward sweep (BFS) power flow algorithm for unbalanced three-phase distribution networks. The proposed power flow algorithm can be implemented in large systems producing accurate results in a small amount of time using as little computational resources as possible. In this version of the algorithm, the network is represented in a tree-like structure, instead of an incidence matrix, avoiding the use of redundant computations and the storing of unnecessary data. An implementation of the method was developed in Python programming language and tested for 3 IEEE feeder test cases (the 4 bus feeder, the 13 bus feeder and the European Low Voltage test feeder), ranging from a low (4) to a very high (907) buses number, while including a wide variety of components witnessed in LV distribution networks.

Keywords: power flow algorithm; data structures; breadth first search; tree-like structure; backward/forward sweep; runtime; distribution network; radial network

# 1. Introduction

In order to properly control and protect a power system, its state must be known so that appropriate actions are taken. Its state is defined as the magnitude and phase of the phase voltages of each bus. The state of the power system is determined through a power flow analysis (PFA). However, the method through which the PFA is performed depends on whether it is applied at the transmission or distribution level of the system. Specifically, the standard Newton-Raphson Algorithm used for PFA of transmission systems cannot be used in many cases on distribution systems due to their topology [1]. Thus, a lot of research has focused on modelling distribution systems and their components for use in PFA. Such research is necessary as distribution networks present characteristics that differ from those of transmission networks. These characteristics include the presence of distributed generation, which is used increasingly due to its lower environmental impact, a radial network structure, single phase or unbalanced consumer loads and high resistance to reactance ratio on lines [1]. Because of the single phase or unbalanced loads, the standard PFA, that assumes a balanced network, is insufficient. In addition to the imbalances present, the number of iterations and convergence of PFA is further affected by the characteristics of the distribution network to be studied, the method used to construct and solve the system equations, as well as the reference frame and transformer model used. A way to tackle this problem is to take advantage of the radial structure of distribution networks



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through a backward-forward sweep (BFS). It involves separating the original system of equations, into two separate systems and solving one, using the last results of the other, until convergence is achieved. Solving for the currents with the voltages given is called the backward sweep (BS) and solving for the voltages with the currents given is called the forward sweep (FS). Of note, BFS can only be used in radial networks, but if the network is indeed radial, as is often the case [2,3], distributed generation like solar panels can be modelled using components presented in this paper, like power-voltage or PQ generators.

A BFS is utilized in [4,5], along with an abc reference frame, which may cause a failure to converge when multiple transformer connections are present [6], while [7] uses BFS along with an admittance matrix to formulate the problem, but in case any system component is altered this matrix must be rebuilt, raising the complexity.

Of note, an improved BFS approach is demonstrated in [8,9], where instead of complex voltages, their real and imaginary parts are calculated instead and then added again to return to complex form. However, it involves solving the circuits twice (both real and imaginary) unlike in the presented method, which only requires solving one circuit in the optimal way. Finally, a dq0 reference frame was implemented in [10], but did not include transformer modelling, and in [11], where Jacobian matrices that increased complexity were used.

Traditional approaches with Jacobian matrices are presented in [12–15], while [16] takes advantage of linear approximations to simplify the Jacobians. References [17,18] use Jacobians in the three sequence networks of an unbalanced network. All these approaches, while effective, require an increased number of computations due to the many matrix operations involved.

Linear approximations are also the basis for [19,20], but these approximation techniques can lack in accuracy or convergence speed in comparison to the BFS algorithms.

The most thorough method for PFA on unbalanced power systems has been presented in [21–23]. It includes the use of a BFS, in combination with the  $\alpha\beta0$  reference frame, which allows modelling both transformers and regulators in a new and unified way similar to transmission lines. The linear system equations are expressed using the node incidence matrix instead of the admittance matrix of the power system. The equations can then be solved using different methods. Furthermore, different load and generators models can be added to the core linear equations depending on the application. For example, the authors of the three afore-mentioned papers chose a trust-region-dopleg algorithm as presented in [24]. The features of this PFA version ensure convergence, even if the initial voltage profile estimate is not optimal. The only problem presented is when PFA is required for large power systems. In these cases, the running time and memory usage are extremely high and render the PFA ineffective. The prohibitive running time and memory usage are the result of very large systems of equations, represented as matrices, which must be solved many times each, to achieve convergence.

The present work adopts the transformer modelling and the BFS approaches presented in [21–23] and improves them in terms of running time and memory requirements. In particular, it modifies the BFS to solve the tree formed by the distribution network, level by level, which leads to accurate results within very short running times and low memory usage, even in systems with a large number of buses. Though the algorithm has been developed in Python, this modification can be used in any programming language and as such is extremely useful. Implementations of the standard and the proposed method are developed and tested in networks of various sizes (smaller network with 4 buses, the largest one with 907 buses), that include a large variety of components that are witnessed in low voltage (LV) distribution systems. The two implementations produce the same accurate results. The largest error observed was  $5 \times 10^{-2}$ . The purpose of testing is to compare the runtime of both methods, witnessing great improvement in large systems. The short running time resulting from the algorithm modification allows problems, like tap optimization, which are difficult to solve efficiently from a computational perspective, to be studied with an exhaustive search of all possible combinations. In conclusion, the paper's main goal is to optimize the BFS approach to solving power flows to the highest possible degree, both in terms of running time and memory usage. The next sections are devoted to the description of the proposed methodology, the presentation of the corresponding results and the elicitation of useful conclusions. In particular, the second section of the paper presents the modelling used for all components included in the power systems under study and the proposed algorithm including the pseudocode, the third one presents the standard and proposed methods' results regarding running time and memory requirements along with a comparison between them, while in the fourth section the results are discussed and conclusions are drawn. Finally, a Nomenclature section explaining the numerous symbols and subscripts found in Section 2 is included before the References section to facilitate the readability and comprehension of the equations presented.

#### 2. Materials and Methods

In order for the PFA equations to be formulated, components of the power system such as generators, loads, transformers, regulators and distribution lines must be properly modelled.

#### 2.1. Per-Unit System Bases

The per-unit system is used for the algorithm as it greatly simplifies power system analysis and allows for a common iterations termination criterion for all power systems.

First, a base power  $S_{\text{base}}$  (per phase) is selected for the whole power system. The base power is equal to the greatest rated power among the components of the power system under study, divided by 3.

Then, a base voltage  $V_{\text{base}}$  is selected for each section of the power system with a different voltage level. This means that n + 1 base voltages are selected, if a power system has n transformers. A base voltage of a section can be equal to the base voltage of a different section of the power system. Each base voltage is equal to the rated voltage of the respective transformer side, divided by  $\sqrt{3}$  (phase voltage).

Using the base power  $S_{base}$  of the system and the base voltages  $V_{base}$  of each section defined by the transformers, the other base quantities for each section can be calculated by:

$$I_{base} = \frac{S_{base}}{V_{base}}$$
(1)

$$Z_{\text{base}} = \frac{V_{\text{base}}^2}{S_{\text{base}}} \tag{2}$$

These per-unit quantities are used for all equations in the following sections of this report. A per-unit quantity can be reverted to its normal value by multiplying it with its base. Many power system components, such as transformers, specify their impedance in the per-unit system, using their rated power and voltage as bases. If the base power S<sub>base,rated</sub> or base voltage V<sub>base,rated</sub> of a component differs from the base power of the power system S<sub>base,system</sub> or the base voltage V<sub>base,new</sub> chosen for its section of the power system, its appropriate per-unit impedance for the given section can be calculated by:

$$\bar{Z}_{pu,new} = \frac{S_{base,system}}{S_{base,rated}} \times \frac{V_{base,rated}^2}{V_{base,section}^2} \times \bar{Z}_{pu,rated}$$
(3)

Each voltage, power and current in an unbalanced power system with three phases is a  $3 \times 1$  vector, where each vector element corresponds to one phase. For example, the voltage of bus i  $\mathbf{\bar{V}}_i$  is:

$$\bar{\mathbf{V}}_{i} = \begin{bmatrix} V_{i,a} \\ \bar{V}_{i,b} \\ \bar{V}_{i,c} \end{bmatrix}$$
(4)

In order to ensure convergence of the PFA [21] and further simplify its equations, the  $\alpha\beta 0$  system is used alongside the per-unit system, instead of the abc system. If a 3 × 1 vector **vector** is given in the abc system, it can be converted to the 3 × 1  $\alpha\beta 0$  system vector **vector**  $\vec{vector}^{\alpha\beta 0}$  by:

$$\overline{\operatorname{vector}}^{\alpha\beta0} = \mathbf{A}^{-1} \times \overline{\operatorname{vector}}$$
(5)

where:

$$\mathbf{A} = \begin{bmatrix} \frac{\sqrt{2}}{\sqrt{3}} & 0 & \frac{1}{\sqrt{3}} \\ -\frac{1}{\sqrt{6}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\ -\frac{1}{\sqrt{6}} & -\frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \end{bmatrix}$$
(6)

and A has the useful property:

$$\mathbf{A}^{-1} = \mathbf{A}^{1} \tag{7}$$

Thus, (5) becomes:  

$$\overline{\text{vector}}^{\alpha\beta0} = \mathbf{A}^{T} \times \overline{\text{vector}}$$

Similarly, a 3 × 1 vector in the  $\alpha\beta 0$  system vector  $\alpha^{\alpha\beta 0}$  can be converted to the 3 × 1 abc system vector vector by:

$$\overline{\text{vector}} = \mathbf{A} \times \overline{\text{vector}}^{\alpha \beta 0} \tag{9}$$

#### 2.3. Generators, Loads and Buses

There are 2 types of generators (a generator at bus i is shown in Figure 1) depending on the control technique applied on them: Power-Voltage (PV) generators and PQ generators.



Figure 1. A generator connected to bus i.

Loads (a load at bus i is shown in Figure 2) represent customer consumption, capacitor banks or distributed generation and are connected in  $Y_g$  or  $\Delta$  configuration. There are three types of loads: PQ loads, I loads, Z loads [25]. For example, data centers, electric vehicles or DC motors are usually represented as PQ loads, street lighting and renewable energy sources are usually represented as I loads. Resistance heating loads and parasitic impedances of distribution are usually represented as Z loads.

(8)



Figure 2. A load connected to bus i.

Buses are the nodes of the power system. There are 3 types of buses i.e., Slack, PV, and PQ ones.

A bus with known voltage magnitude and angle is a slack bus that is described by:

$$\bar{\mathbf{V}}_{\mathbf{i}} = \begin{bmatrix} \bar{\mathbf{V}}_{\mathbf{i},a} \\ \bar{\mathbf{V}}_{\mathbf{i},b} \\ \bar{\mathbf{V}}_{\mathbf{i},c} \end{bmatrix} \text{ remains constant}$$
(10)

Usually the angles of phases a, b, c of the slack bus are 0 degrees (reference), -120 degrees and 120 degrees respectively.

PQ generators generate constant complex power and are described by:

$$\bar{\mathbf{S}}_{\mathbf{G},\mathbf{i}} = \begin{bmatrix} \bar{\mathbf{S}}_{\mathbf{G},\mathbf{i},a} \\ \bar{\mathbf{S}}_{\mathbf{G},\mathbf{i},b} \\ \bar{\mathbf{S}}_{\mathbf{G},\mathbf{i},c} \end{bmatrix} = \begin{bmatrix} \bar{\mathbf{V}}_{\mathbf{i},a} \times \operatorname{conj}(\bar{\mathbf{I}}_{\mathbf{G},\mathbf{i},a}) \\ \bar{\mathbf{V}}_{\mathbf{i},b} \times \operatorname{conj}(\bar{\mathbf{I}}_{\mathbf{G},\mathbf{i},c}) \\ \bar{\mathbf{V}}_{\mathbf{i},c} \times \operatorname{conj}(\bar{\mathbf{I}}_{\mathbf{G},\mathbf{i},c}) \end{bmatrix} \text{remains constant}$$
(11)

where  $\bar{S}_{G,i,ph}$  and  $\bar{I}_{G,i,ph}$  are the generated power and current at each phase,  $ph \in \{a, b, c\}$  of bus i, respectively.

Power-Voltage generators have constant voltage magnitude, generate constant real power and are described by:

$$abs(\bar{\mathbf{V}}_{i}) = \begin{bmatrix} abs(\bar{\mathbf{V}}_{i,a}) \\ abs(\bar{\mathbf{V}}_{i,b}) \\ abs(\bar{\mathbf{V}}_{i,c}) \end{bmatrix} \text{ remains constant}$$
(12)

$$re(\bar{\mathbf{S}}_{\mathbf{G},\mathbf{i}}) = \begin{bmatrix} Re(\bar{S}_{G,i,a}) \\ Re(\bar{S}_{G,i,b}) \\ Re(\bar{S}_{G,i,c}) \end{bmatrix} remains constant$$
(13)

For all loads, their complex power consumption is given by:

$$\bar{\mathbf{S}}_{\mathbf{L},\mathbf{i}} = \begin{bmatrix} \bar{\mathbf{S}}_{\mathrm{L},\mathbf{i},a} \\ \bar{\mathbf{S}}_{\mathrm{L},\mathbf{i},b} \\ \bar{\mathbf{S}}_{\mathrm{L},\mathbf{i},c} \end{bmatrix} = \begin{bmatrix} \bar{\mathbf{V}}_{\mathbf{i},a} \times \operatorname{conj}(\bar{\mathbf{I}}_{\mathrm{L},\mathbf{i},a}) \\ \bar{\mathbf{V}}_{\mathbf{i},b} \times \operatorname{conj}(\bar{\mathbf{I}}_{\mathrm{L},\mathbf{i},b}) \\ \bar{\mathbf{V}}_{\mathbf{i},c} \times \operatorname{conj}(\bar{\mathbf{I}}_{\mathrm{L},\mathbf{i},c}) \end{bmatrix}$$
(14)

and their voltage drop is given by Ohm's Law:

$$\bar{\mathbf{V}}_{\mathbf{L},\mathbf{i}} = \begin{bmatrix} \bar{\mathbf{V}}_{\mathrm{L},\mathbf{i},\mathbf{a}} \\ \bar{\mathbf{V}}_{\mathrm{L},\mathbf{i},\mathbf{b}} \\ \bar{\mathbf{V}}_{\mathrm{L},\mathbf{i},\mathbf{c}} \end{bmatrix} = \begin{bmatrix} \bar{Z}_{\mathrm{L},\mathbf{i},\mathbf{a}} \times \bar{\mathbf{I}}_{\mathrm{L},\mathbf{i},\mathbf{a}} \\ \bar{Z}_{\mathrm{L},\mathbf{i},\mathbf{b}} \times \bar{\mathbf{I}}_{\mathrm{L},\mathbf{i},\mathbf{b}} \\ \bar{Z}_{\mathrm{L},\mathbf{i},\mathbf{c}} \times \bar{\mathbf{I}}_{\mathrm{L},\mathbf{i},\mathbf{c}} \end{bmatrix}$$
(15)

where  $\bar{S}_{L,i,ph}$  and  $\bar{I}_{L,i,ph}$  are the consumed power and current of the load at each phase  $ph \in \{a, b, c\}$  of bus i, respectively and  $\bar{Z}_{L,i,ph}$  is the impedance of the load at phase  $ph \in \{a, b, c\}$  of bus i.

PQ loads consume constant complex power and are described by:

$$\bar{\mathbf{S}}_{\mathbf{L},\mathbf{i}} = \begin{bmatrix} \bar{\mathbf{S}}_{\mathrm{L},\mathbf{i},a} \\ \bar{\mathbf{S}}_{\mathrm{L},\mathbf{i},b} \\ \bar{\mathbf{S}}_{\mathrm{L},\mathbf{i},c} \end{bmatrix} = \begin{bmatrix} \bar{\mathbf{V}}_{\mathbf{i},a} \times \operatorname{conj}(\bar{\mathbf{I}}_{\mathrm{L},\mathbf{i},a}) \\ \bar{\mathbf{V}}_{\mathbf{i},b} \times \operatorname{conj}(\bar{\mathbf{I}}_{\mathrm{L},\mathbf{i},b}) \\ \bar{\mathbf{V}}_{\mathbf{i},c} \times \operatorname{conj}(\bar{\mathbf{I}}_{\mathrm{L},\mathbf{i},c}) \end{bmatrix} \text{ remains constant}$$
(16)

I loads have a constant current magnitude and a constant power factor and are described by:

$$abs(\mathbf{\bar{I}}_{\mathbf{L},i}) = \begin{bmatrix} abs(\mathbf{I}_{\mathbf{L},i,a}) \\ abs(\mathbf{\bar{I}}_{\mathbf{L},i,b}) \\ abs(\mathbf{\bar{I}}_{\mathbf{L},i,c}) \end{bmatrix} \text{ remains constant}$$
(17)

$$\arg(\bar{\mathbf{S}}_{\mathbf{L},\mathbf{i}}) = \begin{bmatrix} \arg(\bar{\mathbf{S}}_{\mathbf{L},\mathbf{i},\mathbf{a}}) \\ \arg(\bar{\mathbf{S}}_{\mathbf{L},\mathbf{i},\mathbf{b}}) \\ \arg(\bar{\mathbf{S}}_{\mathbf{L},\mathbf{i},\mathbf{c}}) \end{bmatrix} \text{ remains constant}$$
(18)

Z Loads have constant impedance and are described by:

$$\bar{\mathbf{Z}}_{\mathbf{L},\mathbf{i}} = \begin{bmatrix} Z_{\mathbf{i},\mathbf{a}} \\ \bar{Z}_{\mathbf{i},\mathbf{b}} \\ \bar{Z}_{\mathbf{i},\mathbf{c}} \end{bmatrix} \text{ remains constant}$$
(19)

Equations (14)–(19) refer to star-connected loads. In the case of delta-connected loads, the subscripts a, b, c are replaced by the subscripts ab, bc and ca, respectively.

#### 2.4. Transformers

Transformers (a transformer at bus i is shown in Figure 3) are not ideal and have losses, which are modelled as an impedance  $\overline{Z}$  equal to:

$$\bar{\mathbf{Z}} = \bar{Z} \times \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix} = \bar{Z} \times \mathbf{Id} = (\mathbf{R} + \mathbf{j} \times \mathbf{X}) \times \mathbf{Id}$$
(20)

where **Id** is the  $3 \times 3$  identity matrix and R and X are the phase resistance and reactance, respectively.



Figure 3. A transformer connected between bus i and bus j.

Primary current  $\mathbf{\bar{I}}_{P}$  is defined as  $\mathbf{\bar{I}}_{ij}$  and secondary current  $\mathbf{\bar{I}}_{s}$  is defined as  $\mathbf{\bar{I}}_{ji}$ . Depending on the transformer connection, different equations are used to relate the primary and secondary voltages with the primary and secondary currents. These equations are given in [21].

## 2.5. Regulators

Only ideal regulators (a regulator at bus i is shown in Figure 4) with zero impedance and configuration Yg-Yg are considered, as only those are used for the IEEE networks under study. The factor r is a given real constant of each regulator that depends on how the windings of the transformer are connected (step-up or step-down) and their turns.



Figure 4. A regulator connected between bus i and bus j.

The equations defining the operation of a regulator with known r for the PFA are [23]:

$$\bar{\mathbf{V}}_{l-n,P}^{\alpha\beta0} = \mathbf{N}_1 \times \bar{\mathbf{V}}_{l-n,S}^{\alpha\beta0} + \mathbf{N}_2 \times \bar{\mathbf{I}}_P^{\alpha\beta0}$$
(21)

where the subscript **l**–**n** denotes line-to-neutral voltages and:

$$\mathbf{N_1} = \mathbf{A}^{-1} \times \begin{bmatrix} \mathbf{r} & \mathbf{0} & \mathbf{0} \\ \mathbf{0} & \mathbf{r} & \mathbf{0} \\ \mathbf{0} & \mathbf{0} & \mathbf{r} \end{bmatrix} \times \mathbf{A}$$
(22)

 $\mathbf{N}_2 = \mathbf{0} \tag{23}$ 

For the regulator currents:

$$\mathbf{0} = \mathbf{N}_3 \times \bar{\mathbf{I}}_P^{\alpha\beta0} + \mathbf{N}_4 \times \bar{\mathbf{I}}_S^{\alpha\beta0} \tag{24}$$

where:

$$\mathbf{N}_{3} = -\mathbf{A}^{-1} \times \begin{bmatrix} \mathbf{r} & \mathbf{0} & \mathbf{0} \\ \mathbf{0} & \mathbf{r} & \mathbf{0} \\ \mathbf{0} & \mathbf{0} & \mathbf{r} \end{bmatrix} \times \mathbf{A}$$
(25)

$$\mathbf{N_4} = -\mathbf{Id} \tag{26}$$

where **Id** is the  $3 \times 3$  identity matrix.

## 2.6. Distribution Lines

Electric power is distributed to the loads through distribution lines (a distribution line between bus i and bus j is shown in Figure 5). Distribution lines are made up of three phase conductors a, b, c and in some cases a neutral conductor. Aside from the voltage drop of each conductor, due to its own current, each conductor also has a voltage drop associated with the current of all other conductors present, due to mutual conductance.



Figure 5. A distribution line connected between bus i and bus j.

The voltage drop of each phase can be determined as a function of the current of each phase:

$$\begin{bmatrix} \mathbf{V}_{i,a} \\ \bar{\mathbf{V}}_{i,b} \\ \bar{\mathbf{V}}_{i,c} \end{bmatrix} = \begin{bmatrix} \mathbf{V}_{j,a} \\ \bar{\mathbf{V}}_{j,b} \\ \bar{\mathbf{V}}_{j,c} \end{bmatrix} + \bar{\mathbf{Z}}_{ij} \times \begin{bmatrix} \mathbf{I}_{ij,a} \\ \bar{\mathbf{I}}_{ij,b} \\ \bar{\mathbf{I}}_{ij,c} \end{bmatrix}$$
(27)

where  $\bar{Z}_{ij}$  is the 6  $\times$  6 impedance matrix of the distribution line.

Multiplying from the left with  $A^{-1}$  and then from the right with A converts (27) into the  $\alpha\beta0$  system:

$$\bar{\mathbf{V}}_{l-n,i}^{\alpha\beta0} = \mathbf{N}_1 \times \bar{\mathbf{V}}_{l-n,j}^{\alpha\beta0} + \mathbf{N}_2 \times \bar{\mathbf{I}}_{ij}^{\alpha\beta0}$$
(28)

where:

$$\mathbf{N}_1 = \mathbf{Id} \tag{29}$$

$$\mathbf{N}_2 = \mathbf{A}^{-1} \times \bar{\mathbf{Z}}_{ij} \times \mathbf{A} \tag{30}$$

Because line-to-line voltages  $\bar{V}_{l-1}$  can be derived from line-to-neutral voltages  $\bar{V}_{l-n}$  through:

$$\bar{\mathbf{V}}_{\mathbf{l}-\mathbf{l}} = \begin{bmatrix} 1 & -1 & 0 \\ 0 & 1 & -1 \\ -1 & 0 & 1 \end{bmatrix} \times \bar{\mathbf{V}}_{\mathbf{l}-\mathbf{n}}$$
(31)

then:

$$\bar{\mathbf{V}}_{l-l,i}^{\alpha\beta0} = \mathbf{N}_1 \times \bar{\mathbf{V}}_{l-l,j}^{\alpha\beta0} + \mathbf{N}_2 \times \bar{\mathbf{I}}_{ij}^{\alpha\beta0}$$
(32)

where:

$$\mathbf{N}_1 = \mathbf{Id} \tag{33}$$

$$\mathbf{N}_{2} = \mathbf{A}^{-1} \times \begin{bmatrix} 1 & -1 & 0 \\ 0 & 1 & -1 \\ -1 & 0 & 1 \end{bmatrix} \times \bar{\mathbf{Z}}_{ij} \times \mathbf{A}$$
(34)

For the distribution line currents:

$$\mathbf{0} = \mathbf{N}_3 \times \bar{\mathbf{I}}_{ij}^{\alpha\beta0} + \mathbf{N}_4 \times \bar{\mathbf{I}}_{ji}^{\alpha\beta0} \tag{35}$$

where:

$$\mathbf{N}_3 = \mathbf{I}\mathbf{d} \tag{36}$$

$$\mathbf{N_4} = \mathbf{Id} \tag{37}$$

Additional impedances between phases and between phases and ground can be easily included as Z loads.

# 2.7. Conversion from 3 to 6 Dimensions

After the equations of transformers, regulators and distribution lines are developed, equations of the form:

$$\begin{bmatrix} \bar{a}_{\alpha} \\ \bar{a}_{\beta} \\ \bar{a}_{0} \end{bmatrix} = \bar{\mathbf{B}} \times \begin{bmatrix} \bar{c}_{\alpha} \\ \bar{c}_{\beta} \\ \bar{c}_{0} \end{bmatrix} + \bar{\mathbf{D}} \times \begin{bmatrix} \bar{e}_{\alpha} \\ \bar{e}_{\beta} \\ \bar{e}_{0} \end{bmatrix}$$

namely Equations (21), (24), (28), (32), (35) and the transformer equations are written in the form:

$$\begin{array}{c} \operatorname{Re}(\bar{a}_{\alpha}) \\ \operatorname{Re}(\bar{a}_{\beta}) \\ \operatorname{Re}(\bar{a}_{0}) \\ \operatorname{Im}(\bar{a}_{\alpha}) \\ \operatorname{Im}(\bar{a}_{0}) \\ \operatorname{Im}(\bar{a}_{0}) \end{array} \end{array} \right] = \left[ \begin{array}{c} \operatorname{Re}(\bar{\mathbf{B}}) & -\operatorname{Im}(\bar{\mathbf{B}}) \\ \operatorname{Im}(\bar{\mathbf{B}}) & \operatorname{Re}(\bar{\mathbf{B}}) \end{array} \right] \times \left[ \begin{array}{c} \operatorname{Re}(\bar{b}_{\alpha}) \\ \operatorname{Re}(\bar{b}_{\beta}) \\ \operatorname{Re}(\bar{b}_{0}) \\ \operatorname{Im}(\bar{b}_{\alpha}) \\ \operatorname{Im}(\bar{b}_{\beta}) \\ \operatorname{Im}(\bar{b}_{0}) \end{array} \right] + \left[ \begin{array}{c} \operatorname{Re}(\bar{\mathbf{D}}) & -\operatorname{Im}(\bar{\mathbf{D}}) \\ \operatorname{Im}(\bar{\mathbf{D}}) & \operatorname{Re}(\bar{\mathbf{D}}) \end{array} \right] \times \left[ \begin{array}{c} \operatorname{Re}(\bar{e}_{\alpha}) \\ \operatorname{Re}(\bar{e}_{\beta}) \\ \operatorname{Re}(\bar{e}_{0}) \\ \operatorname{Im}(\bar{e}_{\alpha}) \\ \operatorname{Im}(\bar{e}_{0}) \end{array} \right] \right] \right]$$

where Re( $\mathbf{\bar{M}}$ ) and Im( $\mathbf{\bar{M}}$ ) of a complex matrix  $\mathbf{\bar{M}} = \begin{bmatrix} \mathbf{\bar{m}}_{11} & \mathbf{\bar{m}}_{12} & \mathbf{\bar{m}}_{13} \\ \mathbf{\bar{m}}_{12} & \mathbf{\bar{m}}_{22} & \mathbf{\bar{m}}_{23} \\ \mathbf{\bar{m}}_{13} & \mathbf{\bar{m}}_{32} & \mathbf{\bar{m}}_{33} \end{bmatrix}$ are defined as  $\begin{array}{c} \text{Re}(\bar{m}_{11}) & \text{Re}(\bar{m}_{12}) & \text{Re}(\bar{m}_{13}) \\ \text{Re}(\bar{m}_{12}) & \text{Re}(\bar{m}_{22}) & \text{Re}(\bar{m}_{23}) \\ \text{Re}(\bar{m}_{13}) & \text{Re}(\bar{m}_{32}) & \text{Re}(\bar{m}_{33}) \end{array} \right] \text{ and } \begin{bmatrix} \text{Im}(\bar{m}_{11}) & \text{Im}(\bar{m}_{12}) & \text{Im}(\bar{m}_{13}) \\ \text{Im}(\bar{m}_{12}) & \text{Im}(\bar{m}_{22}) & \text{Im}(\bar{m}_{23}) \\ \text{Im}(\bar{m}_{13}) & \text{Im}(\bar{m}_{32}) & \text{Im}(\bar{m}_{33}) \end{bmatrix}$ respectively.  $\operatorname{Re}(\bar{a}_{\alpha})$  $\text{Re}(\bar{a}_{\beta})$ A 3 × 1 vector  $\begin{bmatrix} \bar{a}_{\alpha} \\ \bar{a}_{\beta} \\ \bar{a}_{0} \end{bmatrix}$  stated to be converted to a 6 × 1 vector becomes  $\operatorname{Re}(\bar{a}_0)$  $Im(\bar{a}_{\alpha})$  $Im(\bar{a}_{\beta})$  $Im(\bar{a}_0)$  $\operatorname{Re}(\bar{\mathbf{B}})$  $-\text{Im}(\bar{\mathbf{B}})$ ] A 3  $\times$  3 matrix **\bar{B}** stated to be converted to a 6  $\times$  6 matrix becomes  $Im(\bar{\mathbf{B}})$  $\operatorname{Re}(\bar{\mathbf{B}})$ 

# 2.8. Proposed Algorithm

The aim of PFA is to determine the state variables vector x, which is defined as:



where n is the number of buses of the power system. A method to satisfy this purpose is proposed. Bus voltages are line-to-neutral if a neutral exists for the given bus or line-to-line otherwise.

## 2.9. Structure Definition

Instead of an incidence matrix, a tree-like data structure is created to represent the network. This is a crucial change, that has a significant impact on (a) the running time of the algorithm, (b) the memory used to analyze large distribution networks and (c) the complexity of the calculations performed.

The authors of [21] propose an incidence matrix representation, through which a sparse matrix is generated to indicate the network's bus connections. Then, through this incidence matrix, a system of KCL and KVL equations is generated and solved for the whole network, at once. For a radial network with n nodes, a  $6n \times 6n$  matrix is created. Because of the radial property, only  $12 \times (n-1)$  non-zero values are found in this matrix. The remainder of the structure is populated by zeros that have no actual use. In a very large network (more than 800 nodes) one can observe the very large amount of unnecessary memory space that is occupied. Another issue that is encountered in large networks, is the inordinate amount of runtime needed to create this large structure (several concatenations), as well as to perform calculations, utilizing it.

## 2.10. Tree Represantation

The tree representation can be employed to represent a radial network and all its properties with the minimum amount of memory and, if constructed efficiently, in the minimum amount of time. A description of the structure that satisfies this purpose follows: Three classes are defined:

- Bus class; This class is used to represent the buses of the network. The attributes that it can contain are the following:
  - (1) Bus index
  - (2) A list of the loads connected to the bus
  - (3) A list of the generators connected to the bus
  - (4) States of the bus (voltages in rectangular form)
  - (5) A list of bus's descendants
  - (6) Base voltage
- Line Class; This class is used to represent the lines of the network. The attributes that it can contain are the following:
  - (1) Line index
  - (2) A  $6 \times 6$  impedance matrix
  - (3) Current flowing through the line
  - (4)  $N_1$ ,  $N_2$ ,  $N_3$ ,  $N_4$  matrices as described in [21]
  - (5) Base current
- Network Class; This class is used to connect the two previous classes together. It contains two hash tables:
  - (1) A hash table of the buses using as keys the bus index
  - (2) A hash table of the lines using as keys the tuple of the bus objects connected by this line

## 2.11. Algorithm Description

Initialization;

At the Initialization phase of the algorithm, the network representation is generated and the various class attributes are given their initial values. At this stage, the state vectors are initialized with a flat voltage profile.

• BS;

At the BS part of the algorithm, Kirchoff's current law (KCL) is performed in the whole network starting from the leaf nodes and ending at its root.

• FS;

At the FS part of the algorithm, Kirchoff's voltage law (KVL) is performed in the whole network starting from the root node and ending at its leaf nodes.

BS and FS give accurate results only when traversing the tree in a breadthward motion. There are two ways that a tree structure can be traversed, i.e., in a breadthward and in a depthward motion. Depth first traversal cannot give accurate results for BFS, since computations for the previous tree levels must have already taken place to compute the current level correctly.

Error Calculation and Termination

The algorithm terminates, giving the node voltages when a certain error threshold is achieved. The error is calculated from the difference between the states of the current and the previous iteration. The algorithm is also described in flowchart form in Figures 6–8, and in pseudocode form in Section 2.12.



Figure 6. Flowchart of the PFA.



Figure 7. Flowchart of the BS.



Figure 8. Flowchart of the FS.

# 2.12. Pseudocode

Initialization; •

Create Network structure

for every line (ij) in Network:

calculate N1, N2, N3, N4 based on the component existing on the branch and convert them to  $6 \times 6$  matrices

for every bus i in Network:

initialize voltage  $\mathbf{\bar{V}}_{i} = \begin{bmatrix} \bar{V}_{i,a} \\ \bar{V}_{i,b} \\ \bar{V}_{i,c} \end{bmatrix} = \begin{bmatrix} e^{j \times 0^{\circ}} \\ e^{-j \times 120^{\circ}} \\ e^{j \times 120^{\circ}} \end{bmatrix}$  pu, convert it to the  $\alpha\beta 0$  system using

 $\bar{V}_i^{\alpha\beta0}{=}~A^T\times\bar{V}_i$  and subsequently to a 6  $\times$  1 vector

BS; ٠

for every level lvl of the Network starting from the bottom: for every line (ij) in lvl: calculate the  $3 \times 1$  currents vector, I<sub>L</sub>, flowing through the loads of node j using calculations in abc and not in pu by using Equations (16)-(19), convert it to the  $\alpha\beta 0$  system and then to a 6  $\times$  1 vector

calculate the 3  $\times$  1 currents vector,  $I_G$ , produced by the generators of node j using calculations in abc and not in pu by using Equations (11)–(13), convert it to the  $\alpha\beta0$  system and then to a 6  $\times$  1 vector

set  $\mathbf{I}^{\alpha\beta0} = \mathbf{I}_{L}^{\alpha\beta0} + \mathbf{I}_{G}^{\alpha\beta0}$  and  $\mathbf{I}_{D}^{\alpha\beta0} = \begin{vmatrix} 0 \\ 0 \\ 0 \\ 0 \end{vmatrix}$ 

for every descendant k of j:

calculate through KCL the current of line (jk),  $I_{jk}$  in  $\alpha\beta0$  and convert it to a 6  $\times$  1 vector.

using line (ij)'s N<sub>4</sub>, perform the following calculation:  $\bar{I}_D^{\alpha\beta0} = \bar{I}_D^{\alpha\beta0} - N_4 \times \bar{I}_{jk}^{\alpha\beta0}$ set  $\bar{I}^{\alpha\beta0} = \bar{I}^{\alpha\beta0} - \bar{I}_D^{\alpha\beta0}$ 

using line (ij)'s N<sub>3</sub>, perform the following calculation:  $I_{ij} = N_3^{-1} \times I$  FS;

for every level lvl of the Network starting from the top:

for every line (ij) in lvl:

using line (ij)'s  $N_1$ ,  $N_2$  and i's voltages,  $\bar{V}_i$ , as reference, calculate j's voltages:

$$\bar{\mathbf{V}}_{j}^{\alpha\beta0}=N_{2}^{-1}\times(\bar{\mathbf{V}}_{i}^{\alpha\beta0}-\bar{Z}_{ij}\times N_{1}\times\bar{\mathbf{V}}_{i}^{\alpha\beta0})$$

Error Calculation

•

if the difference between the previous and the current state is lower than a certain threshold,  $\varepsilon$ , for every bus: return the voltages of all nodes velse return to BS

### 2.13. Perturbation Sensitivity

The proposed BFS algorithm presents potential sensitivities when it comes to its convergence speed. The first potential factor is the ratio of the real and imaginary parts of the elements of  $\bar{\mathbf{Z}}_{ij}$  of the distribution lines, the second one is the  $Q_L/P_L$  ratio and apparent power  $S_L$  of constant power loads and the third one is the types of loads included in a network, due to different equations being involved in the calculations. The effects of these factors have been examined for a standard BFS algorithm in [26]. Finally, if new transformer connections (open Yg-D) are included in the grid, it may affect the accuracy of the algorithm.

# 3. Results

To compare the widely applicable (Standard) method (as presented by [21]) and the herewith Proposed method of the algorithm, an implementation for each method was developed using the Python 3 programming language and its open-source library NumPy [27,28].

Also, the two methods were tested for their validity using 3 IEEE test feeders [29]:

- the 4-bus feeder
- the 13-bus feeder
- the European LV Test Feeder (907-buses)

Testing on these three networks produced the following mean errors for voltage magnitude and angle:

• For the 4-bus feeder, the mean voltage magnitude error is 0.001085 pu and the mean voltage angle error is 0.065789 degrees.

- For the 13-bus feeder, the mean voltage magnitude error is 0.000035 pu and the mean voltage angle error is 0.003604 degrees.
- For the European LV test feeder, the mean voltage magnitude error is 0.006335 pu and the mean voltage angle error is 0.0036 degrees.

In addition to the mean errors given for these three networks that are under study, the full results for the 5 different transformer configurations of the 4-bus feeder with unbalanced loading are shown in Table 1. Table 1 displays the states (voltage magnitudes and voltage angles) produced by the algorithm, per phase. The iterations needed to end up with this result are also disclosed. In each case, the algorithm terminates producing identical results to those provided by IEEE.

Table 1. Results of the proposed method for step-down unbalanced loading on IEEE 4-bus feeder.

Configuration	$Y_g$ - $Y_g$	Iterations	21
Phase	a	b	c
V <sub>2</sub> (Volt)	7163.706	7110.497	7082.0
$\theta_2$ (degrees)	-0.14	-120.185	119.265
V <sub>3</sub> (Volt)	2305.482	2254.663	2202.783
θ <sub>3</sub> (degrees)	-2.258	-123.625	114.788
V <sub>4</sub> (Volt)	2174.909	1929.87	1832.549
$\theta_4$ (degrees)	-4.124	-126.798	102.843
Configuration	Yg-D	Iterations	12
Phase	a	b	c
V <sub>2</sub> (Volt)	7111.103	7143.654	7111.18
$\theta_2$ (degrees)	-0.205	-120.428	119.537
V <sub>3</sub> (Volt)	3893.741	3973.147	3876.752
θ <sub>3</sub> (degrees)	-2.824	-123.855	115.729
V <sub>4</sub> (Volt)	3422.745	3647.783	3299.48
$\theta_4$ (degrees)	-5.761	-130.299	108.62
Configuration	Y-D	Iterations	12
Phase	a	b	c
V <sub>2</sub> (Volt)	12,358.921	12,347.021	12,300.798
$\theta_2$ (degrees)	29.758	-90.521	149.666
V <sub>3</sub> (Volt)	3896.28	3972.069	3875.026
θ <sub>3</sub> (degrees)	-2.825	-123.827	115.699
V <sub>4</sub> (Volt)	3425.384	3646.242	3297.597
$\theta_4$ (degrees)	-5.762	-130.278	108.582
Configuration	D-Y <sub>g</sub>	Iterations	23
Phase	a	b	c
V <sub>2</sub> (Volt)	12,342.373	12,315.897	12,338.801
$\theta_2$ (degrees)	29.596	-90.352	149.728
V <sub>3</sub> (Volt)	2297.303	2249.416	2216.105
θ <sub>3</sub> (degrees)	-32.427	-154.052	85.48
V <sub>4</sub> (Volt)	2224.267	1800.056	1920.865
θ <sub>4</sub> (degrees)	-32.113	-157.857	72.165
Configuration	D-D	Iterations	11
Phase	a	b	c
V <sub>2</sub> (Volt)	12,341.009	12,370.262	12,301.764
$\theta_2$ (degrees)	29.812	-90.476	149.55
V <sub>3</sub> (Volt)	3901.738	3972.454	3871.361
θ <sub>3</sub> (degrees)	27.202	-93.908	145.736
V <sub>4</sub> (Volt)	3430.623	3647.405	3293.663
$\theta_4$ (degrees)	24.274	-100.364	138.614

Regarding Table 1, for 3 wire buses (Y, D) line-to-line voltages are shown and for 4 wire buses (Yg) line-to-neutral voltages are shown. For example, in the Yg-D configuration the voltages of buses 1 and 2 are line-to-neutral and the voltages of buses 3 and 4 are line-to-line.

In Figure 9 the difference of voltage magnitude results between the IEEE results and proposed method's results (which are equal to the standard method's results in both magnitude and phase) is presented for the 4-bus feeder Yg-Yg configuration (the angle difference between IEEE results and proposed method for all buses is equal to 0).



**Figure 9.** Graph of the difference of voltage magnitude results between the IEEE results and proposed method's results for the 4-bus feeder Yg-Yg configuration.

The two implementations were evaluated based on the time needed to create the data structure representing the network (Structure construction), the runtime of iterations (Iterations) and the total memory used (Memory) in each test case. The results for the three feeders (for one indicative configuration, Yg-Yg, of the 4-bus feeder) are shown in Table 2 and Figures 10 and 11. Only one 4-bus configuration is shown seeing that the structure dimensions in each method are the same for the various configurations, resulting in similar runtime and memory usage among all configurations of the 4-bus feeder. The evaluation took place in a system using the Ubuntu 20.04 operating system and an Intel(R) Core (TM) i7-4510U @ 2.60 GHz CPU.

Table 2. Comparison of the runtime and	memory usage of the standard	l and the proposed method.
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	Standard Method			
	Structure Construction (s)	Iterations (s)	Total (s)	Memory (MB)
4 bus Yg-Yg	0.0132	0.0395	0.0527	2.5283
13 bus Yg-Yg	0.0891	0.2251	0.3142	1.1174
907 bus (European LV test feeder)	178.1690	72.8061	250.9751	949.2373
	Proposed Method			
	Structure Construction (s)	Iterations (s)	Total (s)	Memory (MB)
4 bus Yg-Yg	0.0013	0.0450	0.0463	2.4923
13 bus	0.2351	0.0490	0.2841	2.0818
907 bus (European LV test feeder)	0.9469	2.0362	2.9831	6.3310



Comparison of the total running time between

**Figure 10.** Graph of the total running time of the standard and proposed methods for the 4, 13 and European LV bus feeders.



**Figure 11.** Graph of the total memory usage of the standard and proposed methods for the 4, 13 and European LV bus feeders.

## 4. Discussion

From the results shown in Table 2 and Figures 10 and 11, it is evident that, at least for large networks, the herewith proposed PFA method, which is based on a BFS approach, succeeds in being much less time and memory-consuming than the standard method, which is based on the construction of an incidence matrix.

To summarize, this paper models all power system components in a unified way as presented in previous works by other researchers but proposes a new method to optimize the process of the PFA, subsequently minimizing its running time and memory requirements, especially for large grids, which is very often the case in practice. This is achieved by modifying the network representation from an incidence matrix to a tree-like structure and traversing it, performing only the necessary calculations and no redundant ones.

The method developed has been applied in the IEEE 4-Node Test Feeder with 5 different transformer configurations, in the IEEE 13-Node one and in the European 907-Node one. The results were favorably compared against the results given by IEEE, proving the validity of the method. What is more, the running time and the memory used of the herewith proposed method were compared to the corresponding ones of the standard method available in literature, demonstrating the high efficiency of the new method, resulting in a 98.811% reduction in execution time and a 99.333% reduction in memory use for the large system.

Despite its benefits, the proposed algorithm can be applied in radial distribution networks only. Further development and modifications are envisaged by the present research group for application in networks containing loops, which will address the trend towards meshed distribution networks as well. Future improvements of the method could also include a tap optimization algorithm and a detailed, quantified perturbation sensitivity analysis. **Author Contributions:** Conceptualization, S.P., O.B. and D.R.; methodology, S.P. and O.B.; software, S.P.; validation, S.P.; formal analysis, O.B.; investigation, S.P. and O.B.; resources, S.P. and O.B.; data curation, S.P.; writing—original draft preparation, S.P., O.B. and D.R.; writing—review and editing, D.R. and F.S.; visualization, D.R. and F.S.; supervision, D.R.; project administration, D.R.; funding acquisition, D.R., N.N. and S.V. All authors have read and agreed to the published version of the manuscript.

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#### Abbreviations

I <sub>G,i,ph</sub>	three-phase $3 \times 1$ current vector generated at phase ph of bus i, A
Ī <sub>ij,ph</sub>	three-phase $3 \times 1$ current vector at phase ph of bus i with direction
<i>/</i> 1	from i to j, A
$\bar{\mathbf{I}}_{\text{Li,ph}}$	three-phase $3 \times 1$ current vector demanded by the loads at phase
2)///Р	ph of bus i, A
P <sub>Giph</sub>	three-phase $3 \times 1$ real power vector generated at phase ph of bus i, W
Piinh	three-phase $3 \times 1$ real power vector at phase ph of bus i with direction
ij,pit	from i to j, W
P <sub>L inh</sub>	three-phase $3 \times 1$ real power vector demanded by the loads at phase
L,1,pit	ph of bus i, W
0 <sub>Cinh</sub>	three-phase $3 \times 1$ reactive power vector generated at phase ph of bus i, VAR
O:: nh	three-phase $3 \times 1$ reactive power vector at phase photon of bus i with direction
~ŋ,pn	from i to i, VAR
O <sub>I</sub> ; "h	three-phase $3 \times 1$ reactive power vector demanded by the loads at phase
~L,J,PII	ph of bus i. VAR
<b>Š</b> Ginh	three-phase $3 \times 1$ complex power vector generated at phase ph of bus i, VA
Š <sub>ii nh</sub>	three-phase $3 \times 1$ complex power vector at phase ph of bus i with direction
ij,pit	from i to j, VA
ŠLi ph	three-phase $3 \times 1$ complex power vector demanded by the loads at phase
2,1,1,1	ph of bus i, VA
$\bar{\mathbf{V}}_{1-1\mathrm{i}\mathrm{ph}}$	three-phase $3 \times 1$ line-to-line voltage vector at phase ph of bus i, V
$\bar{V}_{i,ph}$ or $\bar{V}_{l-n,i,ph}$	three-phase $3 \times 1$ line-to-neutral voltage vector at phase ph of bus i, V
Ž <sub>ii</sub>	$3 \times 3$ impedance of distribution line i-j, $\Omega$
Ź,	$3 \times 3$ impedance of transformer, $\Omega$
Subscripts	-
a	at phase a
b	at phase b
С	at phase c
G	generated by generator
i	at bus i
ij	at bus i with direction from i to j
j	at bus j
L	demanded by load
ph	at phase, $ph \in \{a, b, c\}$
Superscripts	
αβ0	vector in the $\alpha\beta 0$ system

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