Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design. It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

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Rapid Publication First decision provided to authors approximately 16.6 days after submission; acceptance to publication is undertaken in 3.6 days (median values for papers published in this journal in the second half of 2021)
Aims and Scope

1. Devices and Technologies
   Advanced technology nodes for low-power; emerging nanomaterials and nanotechnologies; sensors; energy storage devices.

2. Circuits
   Low-power digital circuits for logic; low-power analog/mixed-signal circuits; ultra-low-power RF circuits; ultra-low-power boost converter.

3. Architectures
   Low-power microarchitecture design; asynchronous design; System-on-Chip designs; approximate, brain-inspired and other non-conventional computing; HW/SW co-design; low-power architecture for image, video and graphic processing; SoC FPGA for low-power; low-power deep learning architectures.

4. CAD tools and methodologies
   CAD tools and methodologies for low-power and thermal-aware design; power-aware synthesis and optimization; dynamic power management; energy-efficient software design and software for low-power applications in heterogeneous systems; runtime systems and toolchains.

5. Systems and applications
   Wearable computing; circuits and systems for Internet-of-Things (IoT); deep-learning low-power systems; implantable electronics; bio-sensor circuits; battery-less applications; Cyber-Physical Systems.