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Abstract: The wafer backside grinding process has been a crucial technology to realize multi-layer stacking and chip performance improvement in the three dimension integrated circuits (3D IC) manufacturing. The total thickness variation (TTV) control is the bottleneck in the advanced process. However, the quantitative analysis theory model and adjustment strategy for TTV control are not currently available. This paper developed a comprehensive simulation model based on the optimized grinding tool configuration, and several typical TTV shapes were obtained. The relationship between the TTV feature components and the spindle posture was established. The linear superposition effect of TTV feature components and a new formation mechanism of TTV shape were revealed. It illustrated that the couple variation between the two TTV feature components could not be eliminated completely. To achieve the desired wafer thickness uniformity through a concise spindle posture adjustment operation, an effective strategy for TTV control was proposed. The experiments on TTV optimization were carried out, through which the developed model and TTV control strategy were verified to play a significant role in wafer thickness uniformity improvement. This work revealed a new insight into the fine control method to the TTV optimization, and provided a guidance for high-end grinding tool and advanced thinning process development.

Keywords: total thickness variation; backside grinding; grinding tool configuration; angle adjustment; ultra-precision grinding

1. Introduction

In the three dimension integrated circuits (3D IC) manufacturing field, wafer stacking technology [1] has been developed as an extremely potential method to improve the integrity and computing performance of chips [2]. Out of the demand to realize more wafers stacking layer-by-layer, wafer needs to be thinned from about 780 μ m to less than 10 μ m and be processed to the status with a demanding thickness uniformity [3]. For a better chip thermo-mechanical reliability, the wafer needs to be processed to a proper thickness [4]. Except the high surface accuracy and integrity [5–7], the process efficiency and thickness uniformity are the main requirements for wafer thinning technology. In the industrial production, ultra-precision grinding technology with the workpiece self-rotation characteristic is commonly employed as a total thickness variation (TTV) controllable thinning process with high efficiency [8]. The TTV of the wafer must be controlled less than 1 μ m in the advanced process [9]. It should be noted that the TTV control technology has been a crucial bottleneck for wafer backside grinding machine tool development and process improvement.

In the past decades, many researchers have focused on the theory and method of wafer backside grinding process. Tonshoff et al. [10] proposed an automatic adjustment of grinding tool spindle was necessary and the TTV of an 8 inch wafer was reduced to $1 \mu m$. They just gave the experiment results, but the TTV control method and laws were not revealed. Sun et al. [11,12] defined the TTV shape components to make it more convenient



Citation: Liu, Y.; Tao, H.; Zhao, D.; Lu, X. An Investigation on the Total Thickness Variation Control and Optimization in the Wafer Backside Grinding Process. *Materials* **2022**, *15*, 4230. https://doi.org/10.3390/ ma15124230

Academic Editor: Victor Songmene

Received: 27 May 2022 Accepted: 11 June 2022 Published: 15 June 2022

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Copyright: © 2022 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). for the TTV analysis. Tso et al. [13] focused on the kinematic model to predict wafer TTV and obtained the simulation results that the TTV could be influenced by the wheel infeed rate and the rotation speed ratio between the spindle and chuck table. They suggested that the inclination angle of the spindle needed to be determined appropriately to compensate the concave shape caused by the unsuitable process parameters. Sun et al. [14–16] systematically illustrated the process flow considering the chuck table dressing and wafer grinding, and introduced that the method to control TTV was similar to that for chuck table shape control. Their model predicted the variation trend of wafer TTV characteristic and chuck table shape, and qualitative experiments were conducted to validate the simulation results. Guo et al. [17,18] studied the numerical model for a novel grinding marks and TTV under the given process conditions. They pointed out that the inclination angles had great influence on the wafer TTV features. Tang et al. [19] revealed that the space angles had a significant effect on the wafer shape, while other process parameters could also have slight effect on it. However, it was hard to obtain a wafer flatness which was close to zero.

The effect of TTV control is closely related to the grinding tool configuration design [20]. Sun et al. [11] established the mathematic model to analyze how grinding tool configuration affected TTV shape components, and then proposed an optimized grinder configuration in terms of an easier operation for spindle angle adjustment, through which the "crosstalking" effect between the two TTV components could be eliminated. They pointed out an important criterion to reduce the difficulty of adjusting spindle inclination and achieving a desired TTV results, which required that only one of the TTV components could be affected when tilting the spindle axis around one of the adjustment axes. Zhu et al. [21] developed a two-spindle and three-workstation grinder based on the chuck topography model and the obliquity adjustment device. They adopted the grinder tool configuration which was similar to the optimized one proposed by Sun et al. [11], and they conducted the chuck dressing experiments to obtain a chuck shape as desired. However, more details about the wafer TTV control method and relevant technology requirements were not revealed in their study. Zhou et al. [22–24] developed a backside grinding machine with a horizontal mode. They also established a simulation model to obtain the wafer TTV profile transition laws at different tilt angle combinations. The experiments indicated that a significantly uniform thickness could be reached. Because they adopted the spindle configuration which might cause the "cross talking" effect between the two TTV components, the TTV adjustment mechanism was not revealed clearly.

Generally speaking, the above researchers mainly concentrate on the basic TTV numerical simulation model and obtain several typical TTV shapes under specific process conditions. To the best of our knowledge, no work has been reported to investigate the quantitative relationship between the TTV feature components and spindle inclination angles, so the control strategy for inclination automation adjustment has not been studied. At the same time, several typical grinder configurations and their influence on the TTV control have been compared and analyzed, but there are still more influence laws have not been revealed clearly, which are even different from the previous conclusions in the literature. Furthermore, although the influence factors of TTV are specific, the essential formation mechanism and change rules of TTV have not been revealed. For the requirements to industrial application, the key to realize TTV less than 1 μ m is still unknown, including the strategy of TTV control and the requirements to the accurate inclination angle adjustment. These are also the essential points to the thinning process improvement and high-end equipment research and development.

2. The Mechanism of Angle Adjustment in the Wafer Backside Grinding Process

2.1. The Principle of Wafer Back Grinding Process

Figure 1 illustrates the principle of wafer backside grinding process. The wafer is mounted on a porous ceramics chuck table, and a cup-shaped grinding wheel is held on an aerostatic spindle. The spindle axis is offset by a distance relative to the chuck table

axis to ensure that the wheel edge passes through the chuck table axis. The aerostatic bearings are used for both the spindle and chuck table to obtain high precision rotation accuracy and realize the material removal in the ductile mode [25–27]. In wafer backside grinding process, the spindle and chuck table rotate around their axes simultaneously, and the spindle feeds in the axial direction at a low speed, as shown in Figure 1a. The wafer backside material could be removed by the superimposed movements of the rotation and linear feeding.



Figure 1. Illustration of wafer backside grinding. (a) Diagram of in-feeding mode grinding. (b) Principle of inclination angle adjustment for TTV control.

Before processing the wafer, the chuck table needs to be ground to a cone shape, and the wafer is mounted on it, as shown in Figure 1b. The high accuracy screw mechanism is applied to adjust the inclination angles between the chuck axis and spindle axis, and thus compensate the TTV of the ground wafer. It should be noted that the control method of the chuck table shape is similar to the TTV control [28]. Therefore, this work mainly introduces the simulation model and experimental results on the TTV control. If necessary, the chuck table shape adjustment refers to the same theory and method proposed in this work.

2.2. The Effect of Grinding Tool Configuration on Angle Adjustment Method

As introduced in Section 2.1, the wafer rotates around the chuck table axis and the wheel rotates around the spindle axis. Therefore, a symmetrical wafer surface is formed by the enveloping motion of the grinding wheel edge. The TTV of wafer is usually resolved into two main feature components, i.e., δ_1 and δ_2 [11], as shown in Figure 2. In order to analyze the effect of the angle adjustment on the TTV, the values of δ_1 and δ_2 need to be quantitatively calculated. In Figure 2, δ_1 is equal to the height difference between the wafer center and edge. Line segment *AB* is defined as a reference line, and δ_2 could be obtained by calculating the maximal distance of all the points on wafer section curve to the reference line *AB*. The wafer TTV control is expected to realize by independently adjusting δ_1 and δ_2 without a coupling effect.



Figure 2. Typical TTV shape and its feature components. (**a**) Type 1: The superposition of cone shape and "M" shape. (**b**) Type 2: The superposition of cone shape and "W" shape.

As mentioned above, the spindle axis is offset by a distance relative to the chuck table axis, but the layout between spindle and chuck table is not yet determined. The layout directly affects the angle adjustment strategy. The two TTV feature components are required to be adjusted individually, avoiding coupling change. Figure 3 displays two typical layout schemes of grinding machine, in which the red curve represents the wheel grinding edge. The configuration illustrated in Figure 3a was proposed by Tonshoff et al. [10]. The spindle posture adjustment is carried out by tilting angles around *x*-axis and *y*-axis. Because *x*-axis and *y*-axis are neither perpendicular nor parallel to the arc *OM*, δ_1 and δ_2 can not be adjusted and compensated individually, which has been verified by Sun et al. [11]. This means that it is pretty difficult to decrease TTV to a desirable state just through concise and effective operations. This work would not discuss it deeply.



Figure 3. Diagram of classical grinding tool configurations. (a) Configuration 1: *x*-axis is not perpendicular to the arc *OM*. (b) Configuration 2: *x*-axis is perpendicular to the arc OM.

In Figure 3b, angle adjustment direction is also set around *x*-axis and *y*-axis, but arc *OM* is perpendicular to *x*-axis and parallel to *y*-axis. Based on this layout, Sun et al. [11] established a simulation model and carried out some grinding experiments to study the wafer surface shape. They demonstrated that the angle adjustment along *x*-axis or *y*-axis only affects one of the wafer TTV features, and no coupling effect occurs between δ_1 and δ_2 .

Based on the nature of backside grinding process, the influence laws of space angles (α and β) on TTV feature components (δ_1 and δ_2) could be analyzed and understood more clearly. In fact, it is not completely consistent with the conclusions of the literature and more analysis detail refers to the subsequent contents. Figure 4 shows the three orthographic views of the spindle configuration shown in Figure 3b with the angle adjustment around x-axis. Point O' and point A' are the endpoints of the grinding edge curve. Coinciding with O' and A' individually, another two points, O and A, are set up to describe arc O'A'. An ideal cone is formed by the envelope of *OA* rotating around *z*-axis. Point *B* is an arbitrary point on the wheel grinding edge O'A'. The projection view is made from the direction shown in Figure 4b. As demonstrated in Figure 4d, there is a height difference from point B to the ideal cone surface. When point B coincides with point O' or A', the height difference tends to zero. This means that the grinding edge curve rotating around z-axis would form an approximate cone with a convex feature, different from the ideal cone. In other words, δ_1 and δ_2 would change simultaneously when only α changes, leading to a different conclusion compared with the existing literature [11]. Both chuck table dressing and wafer back grinding process are similar in the selected layout. Through a geometrical analysis for the grinding system, this work preliminarily discusses the new laws of how space angle α affects TTV features. Furthermore, the simulation model will be established to reveal practical TTV control and compensation laws.



Figure 4. Geometrical schematic to analyze the envelope profile of wheel grinding edge after tilting around *x*-axis. (a) Main view. (b) Side view. (c) Vertical view. (d) Projection view.

3. The Wafer Shape Simulation Model and TTV Feature Analysis

3.1. The Mathematical Model for Ground Wafer Surface Shape

Based on the layout scheme illustrated in Figure 3b, a mathematical model for ground wafer surface shape is established to reveal the TTV control principle. The relationship between TTV feature components and space angles is obtained. It can be used for grinding tool design and processing parameter optimization to realize a good wafer thickness uniformity, such as TTV < 1 μ m or lower.

As shown in Figure 3, the wafer circle with radius R_c refers to the outer edge of the wafer. The grinding wheel circle with radius R_w represents the outer edge of the grinding wheel. In order to describe the points on the wafer surface, two coordinate systems, i.e., O(XYZ) and $O_0(X_0Y_0Z_0)$, are established, and their origins are, respectively, set up at the centers of the wafer circle and grinding wheel circle. The wheel axis is almost parallel to the chuck table axis. Due to the requirement of TTV control in backside grinding process, the wheel spindle needs to incline around *x*-axis or *y*-axis to realize a semi-contact condition between the wheel grinding edge and wafer [29].

OM expresses the contact arc between wheel grinding edge and wafer, and its rotational movement around the *z*-axis in the O(XYZ) coordinate system generates the wafer surface shape. The coordinate of arc *OM* in the O(XYZ) coordinate system needs to be obtained when the posture of the spindle is adjusted to control the TTV features. The homogeneous matrix method effectively describes the coordinate transformation between O(XYZ) and $O_0(X_0Y_0Z_0)$ [30]. The position coordinate of the point on arc *OM* is denoted as (x_0, y_0, z_0) in $O_0(X_0Y_0Z_0)$. Based on the configuration in Figure 3, its corresponding point (x, y, z) in O(XYZ) is on the wafer surface and can be expressed as:

$$\begin{bmatrix} x \\ y \\ z \\ 1 \end{bmatrix} = T_{\alpha} \cdot T_{\beta} \cdot T_{xy} \begin{bmatrix} x_0 \\ y_0 \\ z_0 \\ 1 \end{bmatrix}$$
(1)

where matrices T_{α} and T_{β} , respectively, depict the tilt motions of arc *OM* around *x*-axis and *y*-axis; matrix T_{xy} is used to deduce the coordinate translation transformation of arc

OM along *x*-axis and *y*-axis. T_{α} , T_{β} , and T_{xy} are the common matrices in the homogeneous matrix method. All mentioned matrices can be given as:

$$T_{\alpha} = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & \cos \alpha & -\sin \alpha & 0 \\ 0 & \sin \alpha & \cos \alpha & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$$
(2)

$$T_{\beta} = \begin{bmatrix} \cos \beta & 0 & \sin \beta & 0\\ 0 & 1 & 0 & 0\\ -\sin \beta & 0 & \cos \beta & 0\\ 0 & 0 & 0 & 1 \end{bmatrix}$$
(3)

$$T_{xy} = \begin{bmatrix} 1 & 0 & 0 & X_0 \\ 0 & 1 & 0 & Y_0 \\ 0 & 0 & 1 & Z_0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$$
(4)

In Figure 3b, φ_0 is defined as the relevant angle between wheel circle and wafer edge circle, and can be derived as by

$$\varphi_0 = \arcsin \frac{R_c}{2R_w} \tag{5}$$

Then the position coordinate of (X_0, Y_0, Z_0) in O(XYZ) can be obtained by

$$\begin{cases} X_0 = R_w \cos \varphi_0 \\ Y_0 = \frac{1}{2} R_w \\ Z_0 = 0 \end{cases}$$
(6)

Meanwhile, arc *OM* in the $O_0(X_0Y_0Z_0)$ can be expressed as Equation (7). Through discretizing arc *OM* to lots of points and calculating their position coordinates in the O(XYZ), the wafer surface shape and TTV will be obtained accurately by

$$\begin{cases} x_0 = R_w \cos(\pi - \varphi_0 + \varphi) \\ y_0 = R_w \sin(\pi - \varphi_0 + \varphi) \\ z_0 = 0 \end{cases}$$
(7)

where $0 < \varphi < \arcsin \frac{R_c}{2R_w}$.

With the TTV simulation model described in the previous section, a program was developed using software MATLAB (The MathWorks, Inc., Natick, MA, USA). Figure 5 shows five typical wafer surface shapes obtained by the developed simulation model. According to the wafer TTV shape control model, the generatrix of wafer surface shape is almost straight when $\beta = 0$, which is also proved in Figure 5a. When $\alpha = 0$, the conical degree will reduce to zero. Meanwhile, the shape presents a bulge or concave trend at $r = 0.5R_w$ when $\beta > 0$ or $\beta < 0$, which seems to be a 'W' or 'M' shape in Figure 5b,c. When neither α nor β is equal to zero, the wafer TTV shape presents a superposition effect due to the combined action of α and β , as shown in Figure 5d,e.



Figure 5. Wafer TTV simulation results and their topographies of under the typical space angle conditions. (a) $\alpha = -0.006^{\circ}$ and $\beta = 0^{\circ}$; (b) $\alpha = 0^{\circ}$ and $\beta = -0.006^{\circ}$; (c) $\alpha = 0^{\circ}$ and $\beta = 0.006^{\circ}$; (d) $\alpha = -0.006^{\circ}$ and $\beta = -0.006^{\circ}$; (e) $\alpha = -0.006^{\circ}$ and $\beta = 0.006^{\circ}$.

3.2. Effect of Angles Adjustment on TTV Features

First, the influence of adjustment angles α and β on the TTV feature component δ_1 is analyzed. Figure 6a shows the thickness variation of the wafer under different values of α and the wafer TTV can be calculated by it. These curves describe the generatrix shapes of the wafers obtained in the preceding TTV simulation results. The TTV value is the difference between the maximum and minimum thickness, as shown on the curve. β is set to 0.006° as well as α ranges from -0.018° to 0°. With the increasing tilting angle of the wheel axis around *x*-axis, δ_1 shows an upward trend, increasing from 0 to 46.9 µm, as displayed in Figure 6b. The thickness variation of wafer under different values of β is presented in Figure 6c. When β ranges from -0.015° to 0.015° , only the convex or concave degree relative to reference line of wafer shape varies, but the wafer δ_1 keeps constant, equal to 15.7 µm, as shown as Figure 6d. The results show that δ_1 merely depends on the adjustment angle of wheel axis around *x*-axis. In other words, δ_1 could be controlled precisely only by adjusting α .

The relationship between wafer TTV feature component δ_2 and β is analyzed below. Adjustment angle α is set to 0°, and β is set to 0°, ±0.005°, ±0.009°, and ±0.015°. As shown in Figure 7a, the wafer thickness variation shape is axisymmetric and presents an opposite convexity when β has the same value but the sign is reversed. Meanwhile, δ_2 increases linearly with larger absolute value of β , as shown in Figure 7b. Figure 7c exhibits the wafer thickness variation shape under β is 0.006° and α varies from -0.018° to 0°, and δ_2 can be further computed. With the increase in α , δ_2 presents a decreased tendency shown in Figure 7d.



Figure 6. The change rules of δ_1 affected by space angles. Effect of (**a**) α on the thickness variation ($\beta = 0.006^\circ$); (**b**) α on δ_1 ; (**c**) β on the thickness variation ($\alpha = -0.006^\circ$); (**d**) β on δ_1 .



Figure 7. The change rules of δ_2 affected by space angles. Effect of (**a**) β on the thickness variation ($\alpha = 0$); (**b**) β on δ_2 ; (**c**) α on the distance of points to the reference line ($\beta = -0.006^\circ$); (**d**) α on δ_2 .

In order to eliminate the influence caused by β on δ_2 and only analysis the effect of α , β is set to 0° and α ranges from -0.018° to 0°. As shown in Figure 8a,b, the values of δ_1 linearly tend to 46.9 µm, but the values of δ_2 caused by α are lower than 1 µm and are much smaller than the values of δ_1 . As introduced in Section 2.2, δ_2 can be calculated from the distance of all the points on the wafer section curve to the reference line shown in

Figure 8b and it increases linearly with the increasing absolute value of α , illustrated in Figure 8c. However, the numerical simulation results shown in Figure 8b,c are different from the conclusion reported by [11,31]. They pointed out that δ_2 would not be affected by α and the adjustment operation for α and β were absolutely independent, without a coupling effect. In Figure 8d, the comparison between the previous literature and this work is made. Actually, the wafer TTV feature component δ_2 will be affected by both α and β simultaneously. Through both the accurate numerical simulation results in this section and figurative geometric qualitative analysis in Section 2.2, the relationship between wafer TTV features and adjustment angles could be understood more deeply.



Figure 8. The change rules of δ_2 affected by space angles. Effect of (**a**) α on the thickness variation ($\beta = 0^\circ$); (**b**) α on the distance of points to the reference line ($\beta = 0^\circ$); (**c**) α on δ_2 . (**d**) The qualitative analysis and comparison: the change rules of TTV features when α varies between the previous literature and this study.

3.3. Superposition Characteristics of TTV Feature Component

Because δ_2 is affected by both α and β simultaneously, the superposition characteristics of α and β and quantitative relationship between them and δ_2 need to be studied. It will be useful to analyze the wafer surface shape and make an effective strategy to control wafer TTV features. The wafer TTV is simulated under three different adjustment angle conditions as follows:

Condition 1: $\alpha = 0^\circ$, $\beta = 0.006^\circ$; Condition 2: $\alpha = -0.006^\circ$, $\beta = 0.006^\circ$

Condition 2:
$$\alpha = -0.006^{\circ}$$
, $\beta = 0$;

Condition 3: $\alpha = -0.006^{\circ}$, $\beta = 0.006^{\circ}$.

It can be observed from Figure 9a that the wafer thickness variation shape in condition 3 can be considered as the superposed one generated in conditions 1 and 2. Furthermore, δ_1 is only affected by α , which could be verified directly here when comparing the wafer TTV under conditions 2 and 3. Under three conditions above, the different distances from points on wafer section curve to the reference line are calculated and shown in Figure 9b. The green dotted curve represents the linear superposition of distance curves under conditions 1 and 2. It is obvious that the green dotted curve is coincident with the blue curve. This result reveals that δ_2 is finally generated by the effects of both α and β , and there is a clear linear superposition relationship among them.



Figure 9. The verification to the superposition characteristics of δ_2 , under the combination of α and β (when $\beta > 0$). (a) Thickness variation simulation results. (b) The calculation results of the distance of points to the reference line.

Similarly, another groups of α and β combination condition are set to verify the superposition characteristic of δ_2 . The angle parameters are presented as follows:

Condition 4: $\alpha = 0^{\circ}, \beta = -0.006^{\circ};$ Condition 5: $\alpha = -0.006^{\circ}, \beta = 0;$ Condition 6: $\alpha = -0.006^{\circ}, \beta = -0.006^{\circ}.$

From Figure 10, the same conclusions can be conducted that δ_2 is affected by both α and β . In Figure 10b, the blue curve presenting the condition 6 is coincident with the green dotted curve, which is obtained by the distance of points to the reference line under the condition 5 minus that of the condition 4. So δ_2 under condition 6 could be calculated by a linear superposition of values caused by α and β separately. The diversity caused by the two groups of adjustment angle conditions is the δ_2 value and the shape of distance superposition curve. When $\beta = 0.006^\circ$, the distance curves generated by α and β are convex and the final δ_2 value can be calculated by the sum of δ_2 obtained by conditions 4 and 5. When $\beta = -0.006^\circ$, the distance curve caused by α or β presents a contrary shape, which means that β leads to a concave curve compared to the convex curve generated by α . Therefore, the final δ_2 value needs to be calculated by the difference between two δ_2 components. The analysis results for two groups of typical adjustment angle conditions demonstrate that δ_2 is affected by both β and α simultaneously, and can reflect their superposition characteristics.



Figure 10. The verification to the superposition characteristics of δ_2 , under the combination of α and β (when $\beta < 0$). (a) Thickness variation simulation results. (b) The calculation results of the distance of points to the reference line.

3.4. TTV Control Strategy of Wafer Back Grinding

In this work, a simulation model has been established to analyze the quantitative relationship between the wafer TTV and adjustment angles. The wafer TTV depends on the two space angles (α and β) around the *x*-axis and *y*-axis between the wheel spindle and chuck table. The wafer TTV shape feature components, δ_1 and δ_2 , are defined to conduct quantitative analysis. Through simulating several samples under some typical α and β combination conditions, wafer TTV features are calculated and analyzed. The wafer TTV features are the linear superposition effects caused by α and β , and can be controlled accurately by adjusting the two space angles.

According to the simulation and analysis results in Section 3.2, δ_1 is only affected by α , so it is relatively concise to control δ_1 by adjusting α . However, δ_2 is affected by both α and β , an effective strategy needs to be proposed to obtain a desired wafer thickness uniformity. The universal principle for wafer TTV optimization is that:

- 1. If δ_1 is greater than the desired value, the first step is to adjust α and make δ_1 decrease, regardless of whether δ_2 needs to be optimized or not. The second step is to adjust β to compensate for δ_2 caused by both the δ_1 adjustment and the original error of δ_2 .
- 2. If only δ_2 needs to be reduced, just adjust β to compensate for δ_2 to a desired value. Meanwhile, α must remain constant.

3.5. The Analysis of Actual Thinning Process and Related Technology Requirements

In the actual grinding process, the upper surface of chuck table presents a cone shape after dressing. The wafer is adsorbed by a porous vacuum zone and thinned to a preset thickness, as shown in Figure 11a. As the method to control the chuck table shape by adjusting α and β is similar to that of wafer TTV, their adjusting accuracy demand is also the same.

The first step in the chuck table dressing process is to make the wheel spindle axis and chuck table axis parallel. Then we need to adjust α to control the cone height from the center to edge. The chuck table cone height needs to be measured and even re-dressed to the desired value during this process. Re-dressing work is based on the α adjustment, but the angle variation is relative to the current location rather than the parallel position mentioned above. The upper surface of chuck table will present a convex shape here. Finally, we should keep α stable and adjust β according to the measuring results of chuck table shape. In this way, δ_1 is controlled first via changing α and remaining β stable. Then δ_2 is controlled through adjusting β and keeping α stable, which can diminish the δ_2 variation caused by the first step. Otherwise, the coupling variation of δ_2 will happen.



Figure 11. Diagram of the wafer thinning process flow. (a) Chuck table dressing and wafer grinding. (b) The wafer TTV variation trend caused by space angle variation (taking α and its influence for example).

The wafer thickness variation should be very close to zero theoretically after chuck table dressing because the space angles between the spindle and chuck table are not changed deliberately. However, the wafer thickness variation would deteriorate due to the various process parameters, as shown in Figure 11b, including grinding force [32], structure stiffness, grinding wheel type, and other factors. In the industrial production, TTV compensation is necessary and is always carried out according to the measurement results of δ_1 and δ_2 [33,34]. The adjustment laws of TTV features are the same as shown above. For example, δ_1 needs to be compensated by adjusting α as well as δ_2 caused by α variation needs to be concerned. The compensation of δ_2 only needs to adjust β .

In the 3D IC industry, TTV lower than 1 μ m is the technical tendency, and thus the adjustment accuracy of TTV feature components needs to be far lower than 1 μ m. To meet the demand of TTV fine control, the adjustment accuracy of TTV feature components needs to be lower than 0.25 μ m, and the adjustment accuracy of α and β needs to be lower than 0.0001°. The design and development of advanced grinding machines have to follow this technical specification or criterion.

4. Experimental Verification

4.1. Experimental Setup

In order to verify the correctness of the developed model on chuck table shape and wafer TTV control, some grinding experiments, including chuck table dressing and wafer backside grinding, were carried out on the ultra-precision grinding machine tool (Hwatsing Technology Co., Ltd., Tianjin, China Versatile-GP300, developed by the authors' team), as shown in Figure 12a. It should be emphasized that the optimized spindle and chuck table configuration has been adopted to Versatile-GP300. The chuck table contains a porous ceramics structure so that the 12 inch wafer can be mounted tightly on the upper surface of chuck table, as shown in Figure 12b.



Figure 12. Diagram of the experiment setup. (**a**) The ultra-precision grinding machine tool: Versatile-GP300 [35]. (**b**) The internal structure when a wafer is mounted on the chuck table.

The chuck table section shape could be measured in situ by the special measurement device shown in Figure 13a. This measurement device consists of a jig with three fixed legs and height gauges. The height gauge usually adopts a dial indicator or other displacement sensor. On the jig, several mounting holes for the dial indicator are made. The height differences between a specific point on the upper surface of chuck table and the datum plane can be measured. First, the jig with the height gauge is placed on a datum plane (a granite plate with the flatness lower than 1 μ m), and the reading of the height gauge is recorded as a zero position. Then the height gauge is fixed on the jig, and the jig needs to be manually moved to the upper surface of the chuck table [36]. Because there is a height gauge will change and it needs to be recorded. The distances of these mounting holes center to jig center are 0 mm, 25 mm, 50 mm, 75 mm, 100 mm, 125 mm, and 150 mm, respectively, so the chuck table section shape could be obtained through multipoint measurement, as shown in Figure 13b.



Figure 13. The device for chuck table shape measurement. (**a**) Diagram of the in situ measurement. (**b**) The actual measurement on the grinding tool.

The wafer TTV is measured by the thin-film analyzer (F50, Filmetrics Inc., San Diego, CA, USA) as displayed in Figure 14. This instrument employs the principle of infrared interference to ensure a higher accuracy [37,38]. The measuring range and accuracy for silicon material are 7–1000 μ m and 0.4% full scale, respectively. Chuck table dressing and wafer backside grinding experiments are carried out under the process parameters listed in Table 1. In the field of grinding technology, the symbol (#) with a number following (like #600, as shown in Table 1) is used to quantify the size of the abrasive grain in the grinding wheel. The size of the abrasive grain has been stated in Table 1.

Table 1. Process parameters in the experiments of chuck table dressing and wafer grinding.

Process Parameters	Chuck Table Dressing	Wafer Thinning Grinding
Grinding wheel	#600 (Grain size 18–21 μm)	(1) Rough grinding: #325 (Grain size 40–45 μm, resin bonder) (2) Fine grinding: #2000 (Grain size 4–5 μm, resin bonder)
Wheel rotation speed	1700 rpm	3000 rpm
Chuck table rotation speed	40 rpm	280 rpm
Final feeding speed	0.2 μm/s	0.3 μm/s
Coolant	Deionized water, 20 ± 0.5 °C	Deionized water, $20 \pm 0.5~^\circ\mathrm{C}$



Figure 14. The device for wafer TTV measurement and its principle.

4.2. Chuck Table Dressing

In industrial production and application, the height difference from the chuck table center to the edge is always 10–15 μ m, and a cone shape is desirable. So this work set $\alpha = 0.0048^{\circ}$ (corresponding to 12 μ m). Figure 15 shows the comparison between the simulated and measured results of chuck table shape. Although the experiment result presents a slight deviation from simulation results, it is still acceptable because their overall trend is consistent and the error is less than 1 μ m. Meanwhile, the standard deviation for each data point is less than 1 μ m. The experiment results match well with the simulation.



Figure 15. The comparison between the experimental and simulation results of the chuck table shape after dressing process.

4.3. Wafer Back Grinding and TTV Optimization Control

The wafer backside grinding experiments were conducted after chuck table dressing. The wafer was thinned from 775 μ m to about 100 μ m under the processing conditions mentioned in Table 1. The thickness contours are displayed in Figure 16. Figure 16a shows the first piece of wafer thinned directly after chuck table dressing. Its TTV is $3.19 \ \mu m$ and different from the ideal condition. The reason has been explained in Section 3.5. To calculate δ_1 and δ_2 , the measured data at the same radius (radius = 0 mm, 21 mm, 42 mm, 63 mm, 84 mm, 105 mm, 125 mm, and 147 mm) are averaged. For the first wafer, the values of δ_1 and δ_2 are 2.85 µm and 0.21 µm, respectively. The TTV of the first wafer presents a typical cone shape. The δ_2 value is close to zero and does not need to be compensated at this step. Meanwhile, the thickness uniformity along the circumferential direction meets the technical requirement. Then another five pieces of wafer were thinned under different α angle variations set to -0.0002° , -0.0004, -0.0006° , -0.0008° , and -0.001° (negative angle clarifies the adjustment direction). The TTV contours are shown in Figure 16b-f. The thickness average is calculated similar to the first wafer, and their TTV profiles are exhibited in Figure 17a. Obviously, TTV values show a downward tendency, and the desired TTV, less than 1 μ m, is obtained.

The values of δ_1 and δ_2 for each wafer are calculated and shown in Figure 17b. From the second to the fifth wafer, δ_1 decreases linearly and matches well with the simulation results. Meanwhile, the value of δ_2 for each wafer keeps fluctuating in a small range, because the both the original δ_2 and δ_2 variation caused by α are too slight. Many other factors, involving grinding heat, contact deformation, material plastic removal and so on, are not considered in this work. Thus, there exists a deviation of δ_1 between the experimental and simulated data.



Figure 16. The TTV measurement results. (a) The original TTV just after the chuck table dressing; (b–f) TTV compensation results by adjusting α angle.



Figure 17. The quantitative analysis of TTV measurement results. (a) The thickness average values of points at the same radius. (b) The trends of δ_1 and δ_2 with the variation of α angle, and the comparison of δ_1 between the experimental and simulation results.

The values of α and β are controlled by the micro-motion mechanism, and its motion error would lead to an angle error directly affecting the TTV compensation accuracy. When TTV is less than 1 μ m, the related process factors, such as chuck table surface cleaning, grinding heat, coolant, wafer surface initial planarization, machine stiffness, and so on, may be magnified and lead to adverse effects on this scale.

5. Conclusions

This paper focuses on the TTV control theory and method in the wafer backside grinding process. The numerical model is established to obtain wafer TTV shape under an optimized configuration scheme of grinding machine. Based on the simulation model, wafer TTV features can be quantified, and the effects caused by adjustment angles is further analyzed. Chuck table dressing and wafer backside grinding experiments are carried out to verify the accuracy of proposed models.

The wafer TTV shape is determined by the envelope movement of wheel grinding edge and affected by the superposition effect of adjustment angles (α and β). Two feature components (δ_1 and δ_2) are used to quantify the influence of α and β on wafer TTV. The results show that δ_1 is only determined by α . Unlike the previous research, this paper demonstrates that both α and β can impact δ_2 with the linear superposition. This conclu-

sion reveals a new formation mechanism of TTV shape and provides a new insight into the fine control to the TTV optimization. Therefore, a more effective and accurate TTV control strategy can be given: (1) When only δ_2 exceeds the desired value, only β needs to be adjusted. (2) When δ_1 exceeds the allowable value, the principle is to preferentially reduce δ_1 through adjusting α . Then δ_2 needs to be checked and compensated by adjusting β , with the consideration including the original δ_2 error and negative effect caused by α adjusting. The above steps may need to be repeated until neither δ_1 nor δ_2 exceeds the target value.

In the experiment for the chuck table dressing process, a cone-shape section is obtained as expected. The height difference from the center to the edge is 11 μ m and matches well with the simulation analysis. The process results present that the shape of the chuck table can be controlled accurately by adjusting the posture of the spindle. Wafer backside grinding experiments reveal that the wafer TTV can be controlled accurately via adjusting α and β . The δ_1 value is proved to decrease linearly with various α as expected. A wafer with TTV value of 0.86 μ m is achieved, showing a good agreement with simulation results. In summary, this work not only provides meaningful guidance for machine structure design and configuration optimization in the wafer backside grinding process, but also gives an effective solution for improving wafer thickness uniformity to meet the more and more strict requirements in the 3D IC and advanced package field.

For the further research, TTV is required to be diminished to less than $0.5 \,\mu\text{m}$ or lower, so an auto-compensation system for TTV needs to be developed. This auto-compensation system may consist of the in situ TTV measurement device, intelligent recognition algorithms for the TTV features, and auto-adjustment device for controlling the posture of the spindle. With this auto-compensation system, the uniformity of the wafer, and the reliability of the grinding machine tool could be improved further.

Author Contributions: Conceptualization, Y.L., D.Z. and X.L.; Methodology, Y.L. and H.T.; Software, Y.L. and H.T.; Validation, Y.L.; Formal Analysis, Y.L.; Investigation, Y.L. and H.T.; Resources, D.Z. and X.L.; Data Curation, Y.L. and H.T.; Writing—Original Draft Preparation, Y.L.; Writing—Review and Editing, H.T. and D.Z.; Visualization, Y.L. and H.T.; Supervision, D.Z. and X.L.; Project Administration, X.L. and D.Z.; Funding Acquisition, X.L. and D.Z. All authors have read and agreed to the published version of the manuscript.

Funding: The authors gratefully acknowledge the support of the National Natural Science Foundation of China (No. 51991370, No. 51991372 and No. 51991374).

Institutional Review Board Statement: Not applicable.

Informed Consent Statement: Not applicable.

Data Availability Statement: The data supporting reported results by the authors can be sent by e-mail.

Conflicts of Interest: The authors declare no conflict of interest.

References

- 1. De Munck, K.; Vaes, J.; Bogaerts, L.; De Moor, P.; Van Hoof, C. Grinding and Mixed Silicon Copper CMP of Stacked Patterned Wafers for 3D Integration. *MRS Online Proc. Libr.* **2006**, *970*, Y06-03. [CrossRef]
- Tan, C.S.; Reif, R. Silicon multilayer stacking based on copper wafer bonding. *Electrochem. Solid-State Lett.* 2005, 8, G147. [CrossRef]
- 3. Kim, Y.S.; Maeda, N.; Kitada, H.; Fujimoto, K.; Kodama, S.; Kawai, A.; Arai, K.; Suzuki, K.; Nakamura, T.; Ohba, T. Advanced wafer thinning technology and feasibility test for 3D integration. *Microelec-Tron. Eng.* **2013**, *107*, 65–71. [CrossRef]
- Lee, C.C. Effect of wafer level underfill on the microbump reliability of ultrathin-chip stacking type 3D-IC assembly during thermal cycling tests. *Materials* 2017, 10, 1220. [CrossRef] [PubMed]
- Tao, H.; Liu, Y.; Zhao, D.; Lu, X. The material removal and surface generation mechanism in ultra-precision grinding of silicon wafers. *Int. J. Mech. Sci.* 2022, 222, 107240. [CrossRef]
- Tao, H.; Liu, Y.; Zhao, D.; Lu, X. Undeformed chip width non-uniformity modeling and surface roughness prediction in wafer self-rotational grinding process. *Tribol. Int.* 2022, 171, 107547. [CrossRef]

- Liu, Q.; Chen, M.J.; Liao, Z.R.; Xu, D.D.; Cheng, J. On the improvement of the ductile removal ability of brittle KDP crystal via temperature effect. *Ceram. Int.* 2021, 47, 33127–33139. [CrossRef]
- Maeda, N.; Kim, Y.S.; Hikosaka, Y.; Eshita, T.; Kitada, H.; Fujimoto, K.; Ohba, T. Development of sub 10-µm ultra-thinning technology using device wafers for 3D manufacturing of terabit memory. In Proceedings of the 2010 Symposium on VLSI Technology, Honolulu, HI, USA, 15–17 June 2010; pp. 105–106.
- Miyazaki, C.; Shimamoto, H.; Uematsu, T.; Abe, Y.; Kitaichi, K.; Morifuji, T.; Yasunaga, S. Development of high accuracy wafer thinning and pickup technology for thin wafer. In Proceedings of the 2011 IEEE International 3D Systems Integration Conference (3DIC), Osaka, Japan, 31 January–2 February 2012; pp. 1–5.
- Tönshoff, H.K.; Schmieden, W.V.; Inasaki, I.; König, W.; Spur, G. Abrasive machining of silicon. CIRP Ann-Manuf. Technol. 1990, 39, 621–635. [CrossRef]
- 11. Sun, W.; Pei, Z.J.; Fisher, G.R. Fine grinding of silicon wafers: Machine configurations for spindle angle adjustments. *Int. J. Mach. Tools Manuf.* **2005**, 45, 51–61. [CrossRef]
- 12. Sun, W.; Pei, Z.J.; Fisher, G.R. Fine grinding of silicon wafers: A mathematical model for the wafer shape. *Int. J. Mach. Tools Manuf.* **2004**, *44*, 707–716. [CrossRef]
- Tso, P.L.; Teng, C.C. A study of the total thickness variation in the grinding of ultra-precision substrates. J. Mater. Process. Technol. 2001, 116, 182–188. [CrossRef]
- Sun, W.; Pei, Z.J.; Fisher, G.R. Fine grinding of silicon wafers: Effects of chuck shape on grinding marks. *Int. J. Mach. Tools Manuf.* 2005, 45, 673–686. [CrossRef]
- 15. Pei, Z.J.; Strasbaugh, A. Fine grinding of silicon wafers. Int. J. Mach. Tools Manuf. 2001, 41, 659–672. [CrossRef]
- 16. Sun, W.; Pei, Z.J.; Fisher, G.R. A grinding-based manufacturing method for silicon wafers: Generation mechanisms of central bumps on ground wafers. *Mach. Sci. Technol.* **2006**, *10*, 219–233. [CrossRef]
- 17. Guo, X.; Yao, W.; Zhu, X.; Li, Y.; Kang, R. Research on the shape of ground wafer in Back Grinding of Wafer with Outer Rim. *Mater. Sci. Semicond. Process* **2022**, *139*, 106344. [CrossRef]
- Zhu, X.; Yao, W.; Guo, X.; Kang, R.; Ahmad, M.J. The effect of the chuck shape on the wafer topography in back grinding of wafer with outer rim. *Adv. Mech. Eng.* 2021, *13*, 16878140211050488. [CrossRef]
- Tang, K.; Kang, R.; Guo, D.; Song, L. Modeling and investigation on wafer shape in wafer rotational grind-ing method. *Int. J. Adv. Manuf. Technol.* 2013, 64, 707–714. [CrossRef]
- Inui, M.; Ebina, Y.; Maezaki, T. Geometric Simulation of Infeed Grinding Process of Silicon Wafer Using GPU. In Proceedings of the 2018 IEEE 14th International Conference on Automation Science and Engineering (CASE), Munich, Germany, 20–24 August 2018; pp. 1519–1524.
- 21. Zhu, X.L.; Kang, R.K.; Feng, G.; Lv, H.M. Research on topography control of two-spindle and three-workstation wafer grinder. *Proc. Inst. Mech. Eng. Part C–J. Eng. Mech. Eng. Sci.* 2011, 225, 2232–2241. [CrossRef]
- Zhou, L.B.; Eda, H.; Shimizu, J. State-of-the-art technologies and kinematical analysis for one-stop finish-ing of φ300 mm Si wafer. J. Mater. Process. Technol. 2002, 129, 34–40. [CrossRef]
- Zhou, L.; Shimizu, J.; Shinohara, K.; Eda, H. Three-dimensional kinematical analyses for surface grinding of large scale substrate. Precis. Eng.-J. Int. Soc. Precis. Eng. Nanotechnol. 2003, 27, 175–184. [CrossRef]
- 24. Ebina, Y.; Yoshimatsu, T.; Zhou, L.; Shimizu, J.; Onuki, T.; Ojima, H. Process study on large-size silicon wafer grinding by using a small-diameter wheel. *J. Adv. Mech. Des. Syst. Manuf.* **2015**, *9*, JAMDSM0073. [CrossRef]
- Fang, X.; Kang, Q.; Ding, J.; Sun, L.; Maeda, R.; Jiang, Z. Stress distribution in silicon subjected to atomic scale grinding with a curved tool path. *Materials* 2020, 13, 1710. [CrossRef] [PubMed]
- Liu, Q.; Liao, Z.R.; Axinte, D. Temperature effect on the material removal mechanism of soft-brittle crystals at nano/micron scale. Int. J. Mach. Tools Manuf. 2020, 159, 103620. [CrossRef]
- Liu, Q.; Cheng, J.; Yang, H.; Chen, M.J.; Wang, J.H. Effect of tool inclination on surface quality of KDP crystal processed by micro ball-end milling. *Int. J. Adv. Manuf. Technol.* 2020, 99, 2777–2788. [CrossRef]
- Jiang, B.; Zhao, D.; Liu, Y.; Lu, X. Surface figure control in fine rotation grinding process of thick silicon mirror. *Int. J. Adv. Manuf. Technol.* 2018, 98, 771–779. [CrossRef]
- Pei, Z.J.; Strasbaugh, A. Fine grinding of silicon wafers: Designed experiments. Int. J. Mach. Tools Manuf. 2002, 42, 395–404. [CrossRef]
- Chen, C.C.; Hsu, L.S. A process model of wafer thinning by diamond grinding. J. Mater. Process. Technol. 2008, 201, 606–611. [CrossRef]
- Wang, Z.; Yan, Y.; Zhou, P.; Si, L.; Kang, R.; Guo, D. A high-efficient precision grinding for fabricating moderately thick plane mirror (MTPM). *Int. J. Adv. Manuf. Technol.* 2018, *96*, 2559–2566. [CrossRef]
- Sun, J.; Qin, F.; Chen, P.; An, T. A predictive model of grinding force in silicon wafer self-rotating grind-ing. *Int. J. Mach. Tools Manuf.* 2016, 109, 74–86. [CrossRef]
- Eda, H.; Zhou, L.; Nakano, H.; Kondo, R.; Shimizu, J.; Nagao, T. Development of single step grinding sys-tem for large scale φ300 Si wafer: A Total integrated fixed-abrasive solution. *CIRP Ann.-Manuf. Technol.* 2001, 50, 225–228. [CrossRef]
- Uhrmann, T.; Matthias, T.; Wimplinger, M. Recent progress in thin wafer processing. In Proceedings of the 2013 IEEE International 3D Systems Integration Conference (3DIC), San Francisco, CA, USA, 2–4 October 2013; pp. 1–8.

- 35. Hwatsing Technology Co., Ltd. 2021. Available online: http://www.hwatsing.com/xinwenzhongxin/224-2021-09-29-03-31-56 (accessed on 25 May 2022).
- 36. Gao, H.; Tian, Y.B.; Jia, Z.Y. Investigation on the dressing shape of vacuum chuck in wafer rotation grind-ing. *Key Eng. Mater.* **2005**, *291*, 171–176. [CrossRef]
- 37. Kim, E.K. Assessment of ultra-thin Si wafer thickness in 3D wafer stacking. Microelectron. Reliab. 2010, 50, 195–198. [CrossRef]
- 38. Onuki, T.; Ono, R.; Suzuki, A. A thin silicon wafer thickness measurement system by optical reflectmetry scheme using Fourier transform near-infrared spectrometer. *Adv. Mater. Res.* 2013, 797, 549–554. [CrossRef]