Eight-Channel LAN WDM (De)Multiplexer Based on Cascaded Mach–Zehnder Interferometer on SOI for 400GbE

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Abstract: In this paper, we design and experimentally demonstrate an eight-channel cascaded Mach–Zehnder interferometer (MZI) based Local Area Network (LAN) Wavelength Division Multiplexing (WDM) (de)multiplexer with channel spacing of 800 GHz on a silicon-on-insulator. By cascading a three-stage MZI, eight target wavelengths are (de)multiplexed. The length difference of the third-stage MZI delay arms is adjusted so that the output channels skip the guard band. In order to keep the central wavelength of each channel from shifting, we utilize a wide waveguide for the phase delay arm in MZI to achieve large fabrication tolerance, and the multi-mode interference (MMI) couplers as power splitters with weak dispersions. The measurement results of the fabricated device show the precise wavelength alignment over the whole working wavelength range.

Keywords: Local Area Network; Wavelength Division Multiplexing; cascaded Mach–Zehnder interferometer

1. Introduction

Wavelength Division Multiplexing (WDM) is an effective means of improving communication capacity in a limited physical channel, and has been widely adopted in optical communication systems [1]. Local Area Network (LAN) WDM technology with a channel spacing of 800 GHz in the O-band is attractive for inter-data-center application due to their large capacity and long transmission distance. Among several standards for 400 Gbit/s Ethernet (400 GbE) standardized in 2017, 400GBASE-FR8 and -LR8 use the channel correspondence to the LAN WDM [2].

An optical WDM (de)multiplexer can be realized by using different device structures, such as arrayed-waveguide grating (AWG) [3,4], echelle diffraction grating (EDG) [5], or a cascaded Mach–Zehnder interferometer (MZI) [6–8]. Compared to other types of devices, the cascaded MZI has advantages in terms of low insertion loss, spectral flatness, and low crosstalk. At present, most of the research and demonstration of O-band WDM devices focuses on the cascaded MZI coarse WDM (CWDM) [9–13]. However, there are few reports on LAN WDM devices [10,14–16]. In [14], an O-band four-channel (de)multiplexer with low thermal sensitivity for a LAN WDM system is demonstrated experimentally by utilizing silicon nitride (SiN) optical waveguides. In [16], two 4-channel polarization MUXs for 1.28 and 1.3 µm bands are designed. The MUX has 2 × 2 and 2 × 1 MZIs for the first 3200 GHz filters, and is connected by an on-chip polarization splitter rotator (PSR) and a TE1-TM0 mode converter. A tolerance analysis shows that the proposed MUX is robust to the change in waveguide width occurring in the fabrication process.

Because of the narrower channel spacing than CWDM (4.45 nm for LAN WDM to 20 nm for CWDM), it is more difficult to achieve the controllability of the peak wavelength...
position for LAN WDM. Therefore, for 400GBASE-FR8 and -LR8, a fabrication-tolerant MUX with accurate alignment of the wavelength position is highly desired. In this article, we present an 8-channel LAN WDM (de)multiplexer for 400GbE by utilizing three-stage cascaded MZIs. In order to achieve a small size to integrate other devices, we only used a single MZI structure at each stage. The delay arms of each MZI are widened to obtain large fabrication tolerances. The power splitters use the multi-mode interference (MMI) couplers because they are wavelength-insensitive. In this way, an 8-channel LAN WDM (de)multiplexer is manufactured and experimentally demonstrated.

2. Design and Fabrication

The cascaded Mach–Zehnder (de)multiplexer is based on a series of four-port lattice filter building blocks serving as wavelength splitters. To implement an 8-channel LAN WDM, three filter stages with different free spectral ranges and different central wavelengths are connected in a binary tree configuration [17,18]. Figure 1a shows the schematic diagram of the presented 8-channel (de)multiplexer and the demultiplexing process, and the central wavelength and range of eight channels specified in 400 GbE is shown in Table 1. The 8 input wavelength bands are divided into two groups by each MZI filter, and eventually demultiplexed to a dedicated output port.

![Schematic diagram of the 8-channel (de)multiplexer](image)

**Figure 1.** Schematic diagram of (a) the 8-channel (de)multiplexer and the demultiplexing process and (b) the delay arms in MZI. \( W_0 = 0.41 \, \mu m, \, W_1 = 1 \, \mu m, \, L_{dp} = 5 \, \mu m, \, R = 10 \, \mu m. \) (c) The simulation result of the waveguide effective index and group index from 1270 nm to 1315 nm wavelength range. Group index is given by \( n_g = n_{eff}(\lambda) - \lambda \frac{dn_{eff}}{d\lambda}. \)
Table 1. The central wavelength and range of eight channels.

<table>
<thead>
<tr>
<th>Lane</th>
<th>Ch1</th>
<th>Ch2</th>
<th>Ch3</th>
<th>Ch4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Wavelength (nm)</td>
<td>1273.5</td>
<td>1277.9</td>
<td>1282.3</td>
<td>1286.7</td>
</tr>
<tr>
<td>Span (nm)</td>
<td>1272.55–1274.54</td>
<td>1276.89–1278.89</td>
<td>1281.25–1283.27</td>
<td>1258.65–1287.68</td>
</tr>
<tr>
<td>Lane</td>
<td>Ch6</td>
<td>Ch7</td>
<td>Ch8</td>
<td>Ch9</td>
</tr>
<tr>
<td>Wavelength (nm)</td>
<td>1295.6</td>
<td>1300.1</td>
<td>1304.6</td>
<td>1309.1</td>
</tr>
<tr>
<td>Span (nm)</td>
<td>1294.53–1296.59</td>
<td>1299.02–1301.09</td>
<td>1303.54–1305.63</td>
<td>1308.09–1310.19</td>
</tr>
</tbody>
</table>

A single MZI is composed of two couplers and two delay arms. The design of the length difference between two arms is the key of the device, which is related to the change of phase. It will affect both the free spectral region and central wavelength of the MZI. The waveguide–width deviation caused by fabrication errors is a significant factor of phase error. In order to inhibit the negative effect, a 1-µm-wide waveguide is used in the straight waveguide portion of the delay arms [13,19]. All bent waveguides’ widths are chosen to be \( W_0 = 0.41 \) µm and they are connected to the wide waveguides using identical linear tapers with a length of 5 µm to ensure only TE\(_0\) mode propagation, as shown in Figure 1b.

We firstly calculate the waveguide effective index (\( n_{\text{eff}} \)) and group index (\( n_g \)) from 1270 nm to 1315 nm wavelength by using the finite-difference eigenmode (FDE) method (Lumerical MODE). Here, we primarily consider the fundamental TE mode (TE\(_0\)). The waveguide is built on an SOI platform with a 220-nm-thick top silicon layer and a 2000-nm-thick buried oxide layer. The upper cladding is a 3000-nm-thick oxide thin film. Figure 1c shows the simulation result. We then calculate the length difference between two arms (\( \Delta L \)) of each MZI. The 1st-stage MZI needs an FSR equal to two times the channel spacing (4.5 nm) to separate 8-wavelength light into two groups, and the value of the length difference is given by [7]:

\[
\Delta L_1 = \frac{\lambda^2}{2 \cdot \delta \lambda \cdot n_g}
\]

Then, the 2nd-stage (3rd-stage) MZI continues to separate the wavelength channels with an FSR equal to four (eight) times the channel spacing. Therefore, the length difference between two arms is \( \Delta L_1 / 2 \) of the 2nd-stage MZI and \( \Delta L_1 / 4 \) of the 3rd-stage MZI similarly. Since 1291 nm is not in the channel wavelength range required for the LAN WDM, the FSR of the 3rd-stage MZI that filters the 1st and 8th channels should be sixteen times the channel spacing, and the length difference is \( \Delta L_1 / 8 \), to skip this wavelength range. Finally, we adjust the wavelength position of the pass bands. These should be moved by a fraction of one FSR to separate channels. A shift of one full FSR is achieved by adding a delay arm length difference of [7]:

\[
\Delta L_{\text{shift}} = \frac{\lambda}{n_{\text{eff}}}
\]

The correct setting and total length of the arm length difference for all MZIs are summarized in Table 2.

Table 2. Parameters for the calculation of the delay line lengths.

<table>
<thead>
<tr>
<th>MZIs</th>
<th>( \Delta L )</th>
<th>Total Length (µm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1st</td>
<td>( \Delta L_1 )</td>
<td>48.4</td>
</tr>
<tr>
<td>2st_A</td>
<td>( \Delta L_1 / 2 )</td>
<td>24.2</td>
</tr>
<tr>
<td>2st_B</td>
<td>( \Delta L_1 / 2 + 0.75 \Delta L_{\text{shift}} )</td>
<td>24.53</td>
</tr>
<tr>
<td>3rd_A</td>
<td>( \Delta L_1 / 4 )</td>
<td>12.21</td>
</tr>
<tr>
<td>3rd_B</td>
<td>( \Delta L_1 / 4 + 0.25 \Delta L_{\text{shift}} )</td>
<td>12.156</td>
</tr>
<tr>
<td>3rd_C</td>
<td>( \Delta L_1 / 4 + 0.125 \Delta L_{\text{shift}} )</td>
<td>12.264</td>
</tr>
<tr>
<td>3rd_D</td>
<td>( \Delta L_1 / 4 + 0.375 \Delta L_{\text{shift}} )</td>
<td>12.264</td>
</tr>
</tbody>
</table>

A broadband coupler is another important issue in this design. All the splitters of MZIs are based on 2 × 2 multi-mode interference (MMI) couplers with a 50:50 splitting.
ratio [9]. We optimize the MMI parameters by using the eigenmode expansion (EME) method (Lumerical MODE). The simulation structure model is shown in Figure 2 and the parameters are chosen to be as follows: $L_{\text{MMI}} = 54.2 \, \mu\text{m}$, $W_{\text{MMI}} = 6 \, \mu\text{m}$, $L_{\text{taper}} = 12 \, \mu\text{m}$ and $W_{\text{taper}} = 1.8 \, \mu\text{m}$.

![Figure 2. Schematic diagram of 2 × 2 MMI and the simulation result of mode field distribution.](image)

To verify the feasibility of the design, we utilize an optical circuit solver (Lumerical INTERCONNECT) to construct an 8-channel cascaded MZI-type (de)multiplexer with the parameters calculated before. It is necessary to ensure that the center wavelength of each stage is aligned, which has a great impact on the insertion loss and crosstalk of the device. The simulation result is shown in Figure 3. It can be seen that all channel wavelengths are accurately aligned by the results.

![Figure 3. Measured transmission of the 8-channel (de)multiplexer.](image)

### 3. Device Characterization

Figure 4 shows the micrograph of the fabricated device with a footprint of $650 \, \mu\text{m} \times 470 \, \mu\text{m}$. An 8-inch silicon-on-insulator wafer with a 220-nm-thick top silicon layer and a 2000-nm-thick buried silicon dioxide layer is used to fabricate the device. The patterns are defined by 193 nm deep ultraviolet photolithography. Inductively coupled plasma etching is employed to fully etch the silicon top layer. A 2000-nm-thick silicon dioxide layer is deposited on the silicon layer by plasma-enhanced chemical vapor deposition (PECVD).
Figure 4. The microscope images of the fabricated 8-channel LAN WDM (de)multiplexer.

A broadband amplified spontaneous emission (ASE) source and an optical spectrum analyzer (OSA) were used to characterize the fabricated device. The light coupling from ASE to chip and from chip to OSA is edge coupling, and then the coupling loss is eliminated from the test results by normalization. Figure 5 plots the measured transmission spectra at all eight outputs of the device. The measurement results indicate that the eight-channel central wavelengths are well aligned to the standard wavelength range. The IL of the central wavelength varies between 2.77 dB and 3.77 dB. Higher insertion loss is mainly caused by the following reasons. The first reason is that MMI has a large insert loss. According to our measurement, the loss of each MMI is approximately 0.4 dB. In future work, we plan to use bent directional couplers to reduce the loss and footprint while ensuring broadband characteristics [20,21]. The deviation of the center wavelength of the device will also introduce additional loss. As can be seen from Figure 5, the deviation of ch6 is 0.7 nm (1294.9 nm to 1295.6 nm), which introduces 0.4 dB loss at the standard center wavelength. More detailed test results are shown in Table 3.

Figure 5. Measured transmission of the 8-channel (de)multiplexer.
Table 3. Performance of the 8-channel (de)multiplexer.

<table>
<thead>
<tr>
<th>Lane</th>
<th>Ch1</th>
<th>Ch2</th>
<th>Ch3</th>
<th>Ch4</th>
</tr>
</thead>
<tbody>
<tr>
<td>IL (dB)</td>
<td>−3.45</td>
<td>−3.30</td>
<td>−3.57</td>
<td>−2.77</td>
</tr>
<tr>
<td>Crosstalk (dB)</td>
<td>−12.54</td>
<td>−14.13</td>
<td>−13.91</td>
<td>−15.15</td>
</tr>
<tr>
<td>Deviation (nm)</td>
<td>−0.4</td>
<td>0</td>
<td>−0.1</td>
<td>+0.06</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Lane</th>
<th>Ch6</th>
<th>Ch7</th>
<th>Ch8</th>
<th>Ch9</th>
</tr>
</thead>
<tbody>
<tr>
<td>IL (dB)</td>
<td>−3.77</td>
<td>−3.06</td>
<td>−3.08</td>
<td>−3.10</td>
</tr>
<tr>
<td>Crosstalk (dB)</td>
<td>−16.08</td>
<td>−16.42</td>
<td>−14.43</td>
<td>−15.06</td>
</tr>
<tr>
<td>Deviation (nm)</td>
<td>−0.7</td>
<td>+0.08</td>
<td>+0.23</td>
<td>+0.1</td>
</tr>
</tbody>
</table>

To evaluate inter-die distribution, the measured spectra for all output channels located in five different areas across the 8-inch wafer were superimposed, as shown in Figure 6. Figure 7 shows the superimposed results of the 8-channel spectra and each color represents the test results of a chip, and the statistical distribution in terms of insertion loss and deviation for all channels is shown in Table 4. It can be seen that the range of changes in loss is large, which may be due to coupling errors. The average insertion loss of all channels is 2.8 dB, which is mainly caused by the loss of MMI, and is consistent with our previous test results. The center wavelength of each channel is within the specified wavelength, and the deviation of each channel is less than 1 nm. This proves the reproducibility and fabrication tolerance of our device. At the same time, the small device footprint means that it easy to integrate with other devices. These features demonstrate that our device has broad prospects in the application of 400GbE silicon optical modules.
Table 4. Statistical distribution in terms of insertion loss and deviation for all channels.

<table>
<thead>
<tr>
<th>Lane</th>
<th>IL (dB)</th>
<th>Deviation (nm)</th>
<th>Lane</th>
<th>IL (dB)</th>
<th>Deviation (nm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ch1</td>
<td>$-3.25 \pm 0.57$</td>
<td>$-0.4 \sim +0.13$</td>
<td>Ch6</td>
<td>$-2.56 \pm 0.5$</td>
<td>$-0.07 \sim +0.12$</td>
</tr>
<tr>
<td>Ch2</td>
<td>$-2.88 \pm 0.42$</td>
<td>$-0.32 \sim -0.05$</td>
<td>Ch7</td>
<td>$-2.60 \pm 0.55$</td>
<td>$-0.04 \sim +0.01$</td>
</tr>
<tr>
<td>Ch3</td>
<td>$-3.13 \pm 0.79$</td>
<td>$-0.52 \sim +0.05$</td>
<td>Ch8</td>
<td>$-2.75 \pm 0.4$</td>
<td>$-0.59 \sim +0.32$</td>
</tr>
<tr>
<td>Ch4</td>
<td>$-2.61 \pm 0.44$</td>
<td>$-0.28 \sim +0.06$</td>
<td>Ch9</td>
<td>$-2.88 \pm 0.8$</td>
<td>$-0.02 \sim +0.14$</td>
</tr>
</tbody>
</table>

Figure 7. Measured transmission of five 8-channel (de)multiplexer across an 8-inch wafer. The range between the red dotted lines is the wavelength range of each channel.

4. Conclusions

In conclusion, we have theoretically and experimentally demonstrated an 8-channel LAN WDM (de)multiplexer based on the cascaded MZIs, which is designed for the 400GBASE-FR8 and 400GBASE-LR8 norm. The eight-channel central wavelengths of this device are accurately aligned with the target wavelength range. Five multiplexers are characterized with the superimposed transmission spectra to prove the reproducibility and fabrication tolerance. The measurement results show that the insertion loss of each channel is approximately 2.8 dB, and the deviation is less than 1 nm. All these features make our proposed (de)multiplexer promising for 400GBASE-FR8 and 400GBASE-LR8 applications.

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Data Availability Statement: Not applicable.

Conflicts of Interest: The authors declare no conflicts of interest.

Abbreviations

The following abbreviations are used in this manuscript:

- **MZI**: Mach–Zehnder interferometer
- **LAN**: Local Area Network
- **WDM**: Wavelength Division Multiplexing
- **MMI**: multi-mode interference
- **AWG**: arrayed-waveguide grating
- **EDG**: echelle diffraction grating
- **CWDM**: Coarse Wavelength Division Multiplexing
- **SiN**: silicon nitride
- **PSR**: polarization splitter rotator
- **FDE**: finite-difference eigenmode
- **EME**: eigenmode expansion
- **IL**: insertion loss
- **PECVD**: plasma-enhanced chemical vapor deposition
- **ASE**: amplified spontaneous emission
- **OSA**: optical spectrum analyzer

References


